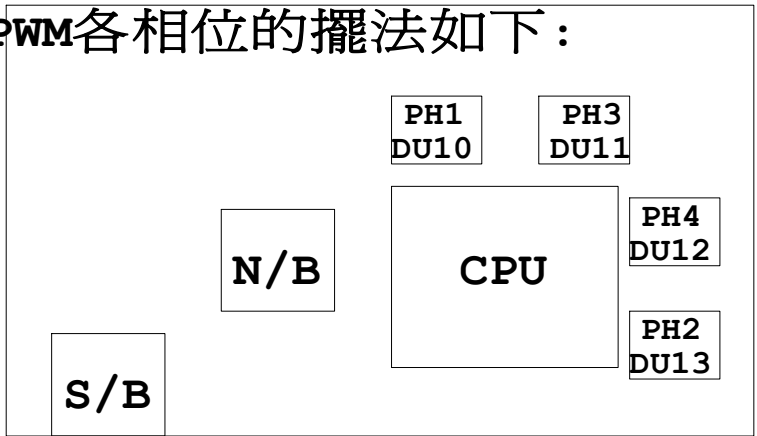


Model Name: GA-EP43T-UD3L REV 1.1

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	TABLE LIST
05	P4 LGA775 A
06	P4 LGA775 B,D
07	P4 LGA775 C
08	P4 L775 E,F,G,H
09	GMCH-Eaglelake HOST
10	GMCH-Eaglelake DDRII
11	GMCH-Eaglelake PCI E, DMI
12	GMCH-Eaglelake INT VGA
13	GMCH-Eaglelake GND
14	GMCH-Eaglelake PWR
15	DDR3 CHANNEL A 1,2
16	DDR3 CHANNEL B 1,2
17	DDR3 TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH10 DMI, PCI, USB
20	ICH10 GPIO, CTRL
21	ICH10 SATA, FAN PWM
22	ICH10 VCC, GND
23	CLOCK-ICS9LPRS914
24	PCI SLOT 1, 2, PCIEX1 1~4
25	ITE8718/GB,RESET DRIVE
26	COM LPT, -PROHOT,DYNAMIC,RUSB
27	BIOS,CI,HWM,KB/MS

SHEET	TITLE
28	AZALIA ALC888
29	AUDIO JACK
30	VCORE PWM ISL6334CRZ
31	DISCRETE1 POWER,FAN CTRL
32	ATX POWER
33	JMicron JMB368
34	LAN REALTEK RTL8111C
35	FRONT PANEL,FUSB,FDD
36	TPM I/F-1.2

PWM各相位的擺法如下：

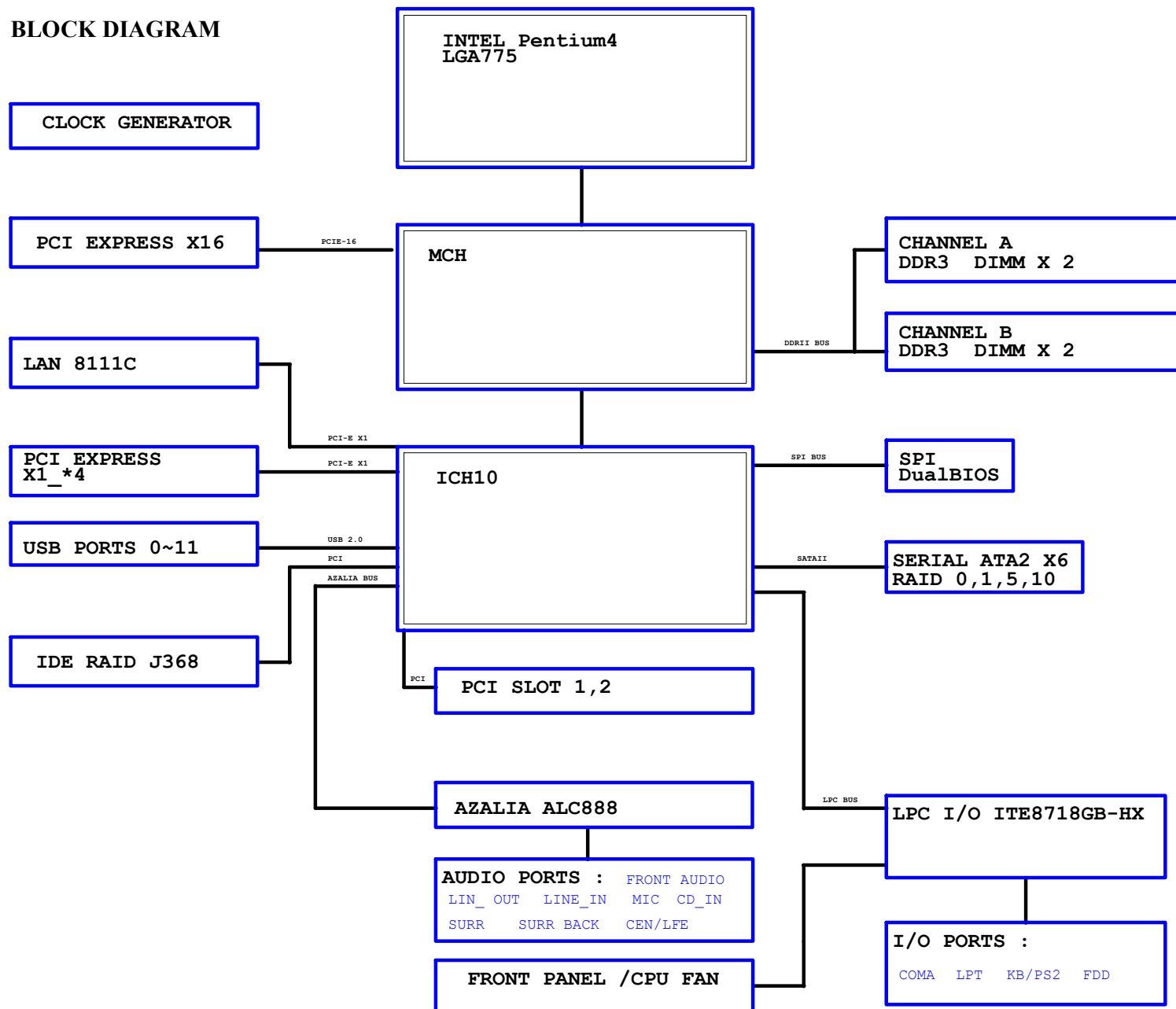


### Component value change history

Data	Change Item	Reason
97/04/01 EBOM: 01A	1. P43 CHIPSET E-BOM	
97/04/15 EBOM: 02	1. 修改LED的OWER及阻值;DEL R484,DR78. ADD DR79,,R348	
	2. ADD DR80,R300 10-->49.9,C158,LBC43 0ohm-->100FF for EMI	
	3. del Q3,Q4,BC11,BC9,R42,R15,PCI_BT1,PCI_BT2,R166,R168	
97/04/28 EBOM: 10A	1. DDR2 VOLTAGE 1.83 --> 1.9V --> 2.0V --> 2.1V .....-->2.5V	
97/05/09 PBOM: 10B	1. DR59,DR60 14K---->549ohm,del DR69	
	2. ADD U9(uP6262),R436,BC133 FOR CPU 超頻	
97/05/21 PBOM: 10C	1. ICH,MCH PCI-E ,JM368的RX,TX串電容BOM 0.1u/Y5V-->0.1u/X7R,RTC RTCVDD -->X7R	
	2.ADD U6 FOR DDR TURN ON 2.1V ISSUE	
97/06/4 PBOM: 10D	1.DEL Q107,R620,ADD R621	
	2.Q49(BAT54C) 限用 DII	
97/06/18 PBOM: 10E	1.ADD MB_ID R283,DEL R282,Q87,Q91,R452,R498,R499,R500 FOR VTT_GMCH 1.2V	
	2.C197 0.1u/Y5V--->X7R	
	3.R300 49.9--->100 ohm ,C158 Y5V--->X7R for USB	
	4.DC20 0.01u--->1nf FOR CPU PSI ISSUE	
97/08/07 EBOM: 20A	1.CPU 改為 SMART FAN	
	2.L4,L7 CHOKE Footprint Change "CHOKE1U2-20A-1PQN"	
	3.獨立南橋1.1V 的電壓	
	4. ADD GPIO37 FOR LOAD LINE CALIBRATION	
	5.J368 改為 1.8V;R209=100 OHM, ADD R640 FOR MB_ID2	
97/08/08 EBOM: 30A	1.J368 改為 1.8V;47--->44.2	
	2.TO252---改為POWER PACK	
97/08/26 EBOM: 10A	1.DDR2換成DDR3 2.換NB /SB HEAT SINK	
97/10/15 PBOM: 10B	1.CHANGE SB HEAT SINK FOR DDR3專用 2.R414 102K-->105K;R384 1.27k---->1.3k for 電壓調	
	3. FDD改為白色	
98/02/20 PBOM: 10C	1.100UF 統一料號	
98/05/18 PBOM: 10D	1.Backup bios R56 pull-high 1k--->330 ohm; 移除SST BIOS	
98/06/16 PBOM: 10E	1.藍、白 DIMM slot;白色IDE	
98/11/12 EBOM: 11A	1.GA-EP43T-UD3L-1.1;Add EUP function,audio co-lay 892 (同EP45T-UD3LR SHARE PCB)	
98/11/27 PBOM: 11A	1.GA-EP43T-UD3L-1.1轉P-BOM ,ADD PACKAGE	

[illegible]

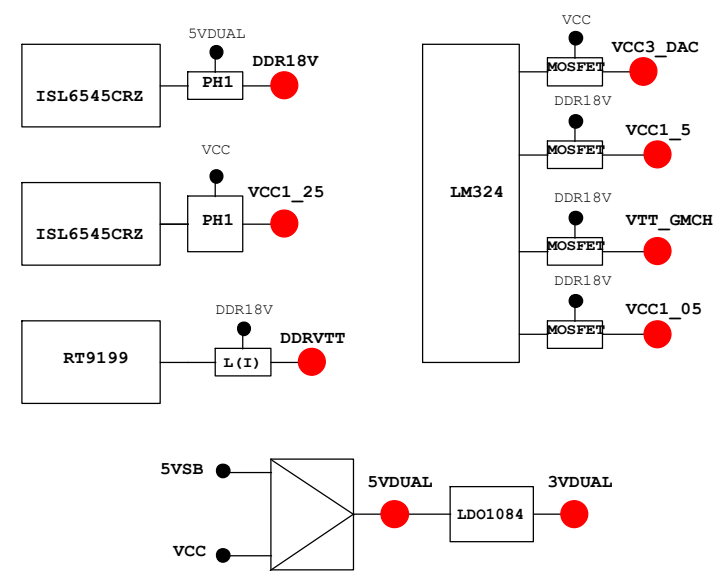
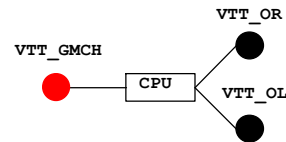
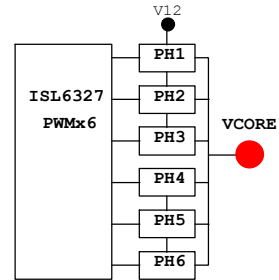
# BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

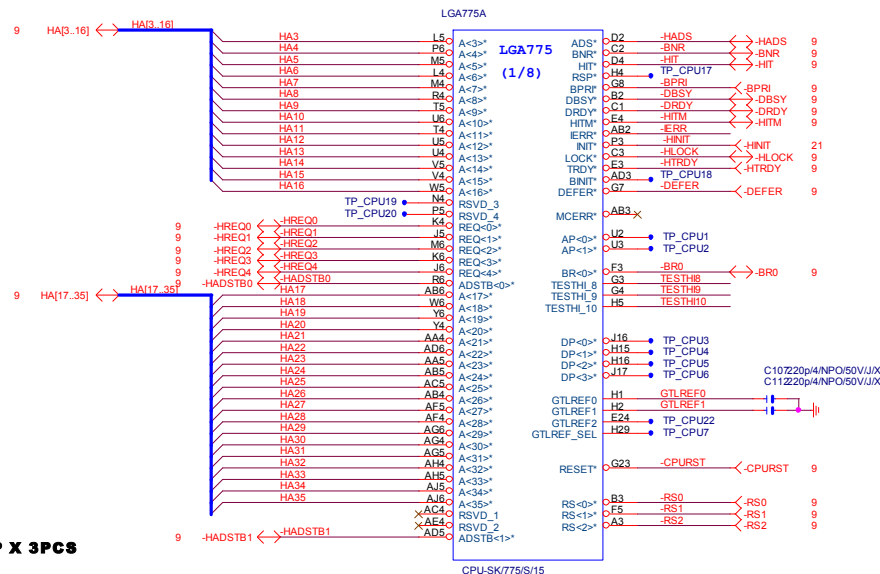
PIN NAME	PWR WELL	AFTER/ BLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8 (DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID0	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	LAN_DISABLE (STP_PCI-)	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	SATA1GP	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WF0	P/U 1K 3VCL
GP21	MAIN	IN	SATA0GP	P/U 8.2K VCC3
GP22	MAIN	IN	SCLOCK	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	CLGPIO0	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2 (STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27 (EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	PWR_LED (EL_STATE1)	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL_BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL_BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	SATACLKREQ#	N/A
GP36	MAIN	IN	SATA2GP	P/U 8.2K VCC3
GP37	MAIN	IN	SATA3GP	P/U 8.2K VCC3
GP38	MAIN	IN	SLOAD	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:6 PHASE PWM--ISL6327CRZ

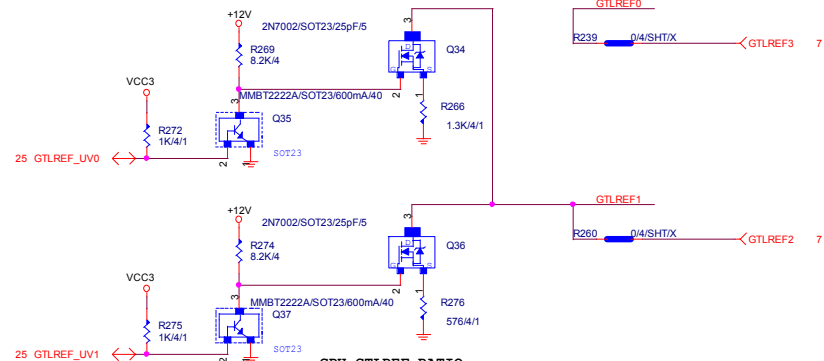
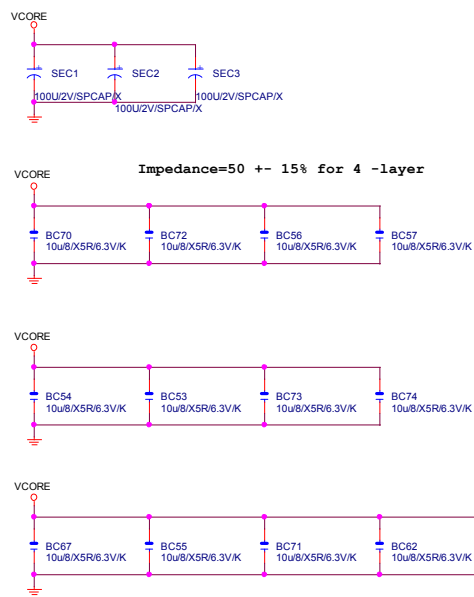


Gigabyte Technology			
Title			
TABLE LIST			
Size B	Document Number		Rev
GA-EP43T-UD3L		1.1	
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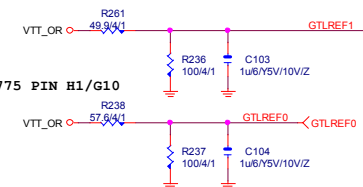
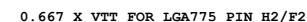
HA/REQ:50歐姆+-15% [ 4/11 ]  
ADSTB:50歐姆+-15% [4/14]



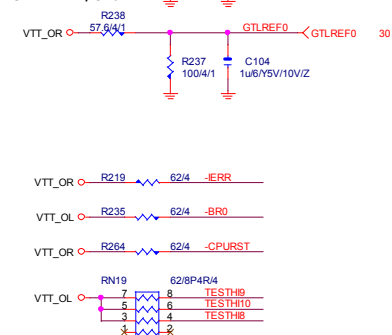
**SP-CAP X 3PCS**



CPU GTLREF RATIO		
GTLREF UV0	GTLREF UV1	Ratio Set
HIGH	HIGH	0.67
LOW	HIGH	0.65
HIGH	LOW	0.63
LOW	LOW	0.615



0.635 X VTT FOR LGA775 PIN H1/G10

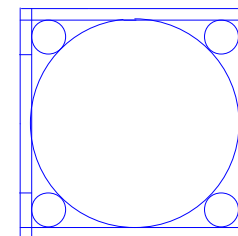


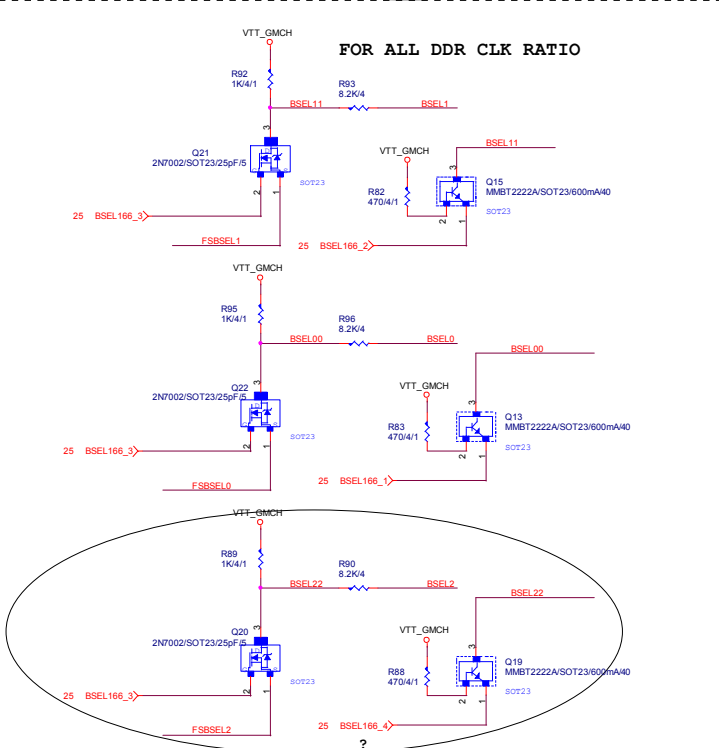
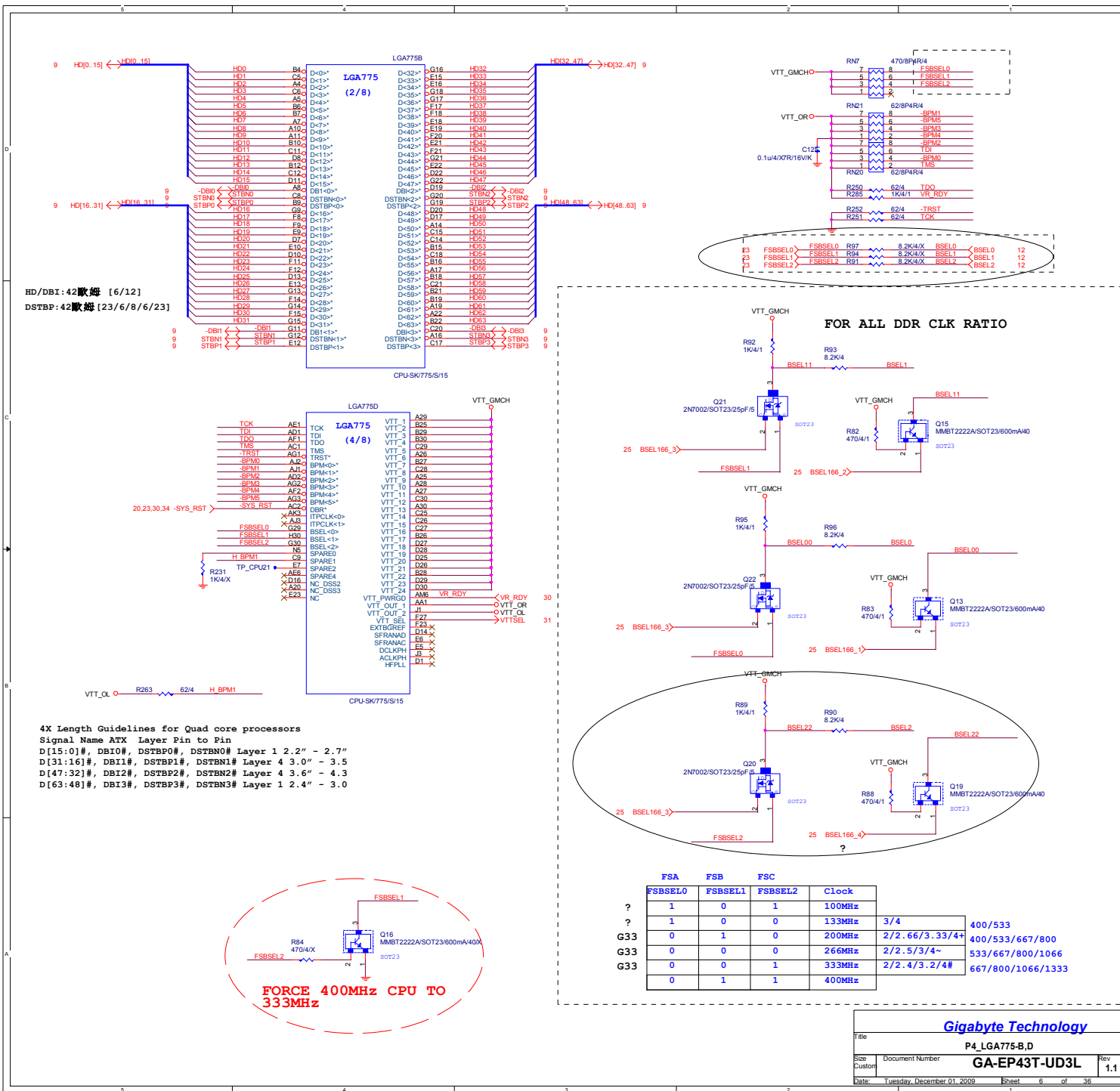
VTT OR 

VTT OL ○ R235 62/4 -BR0

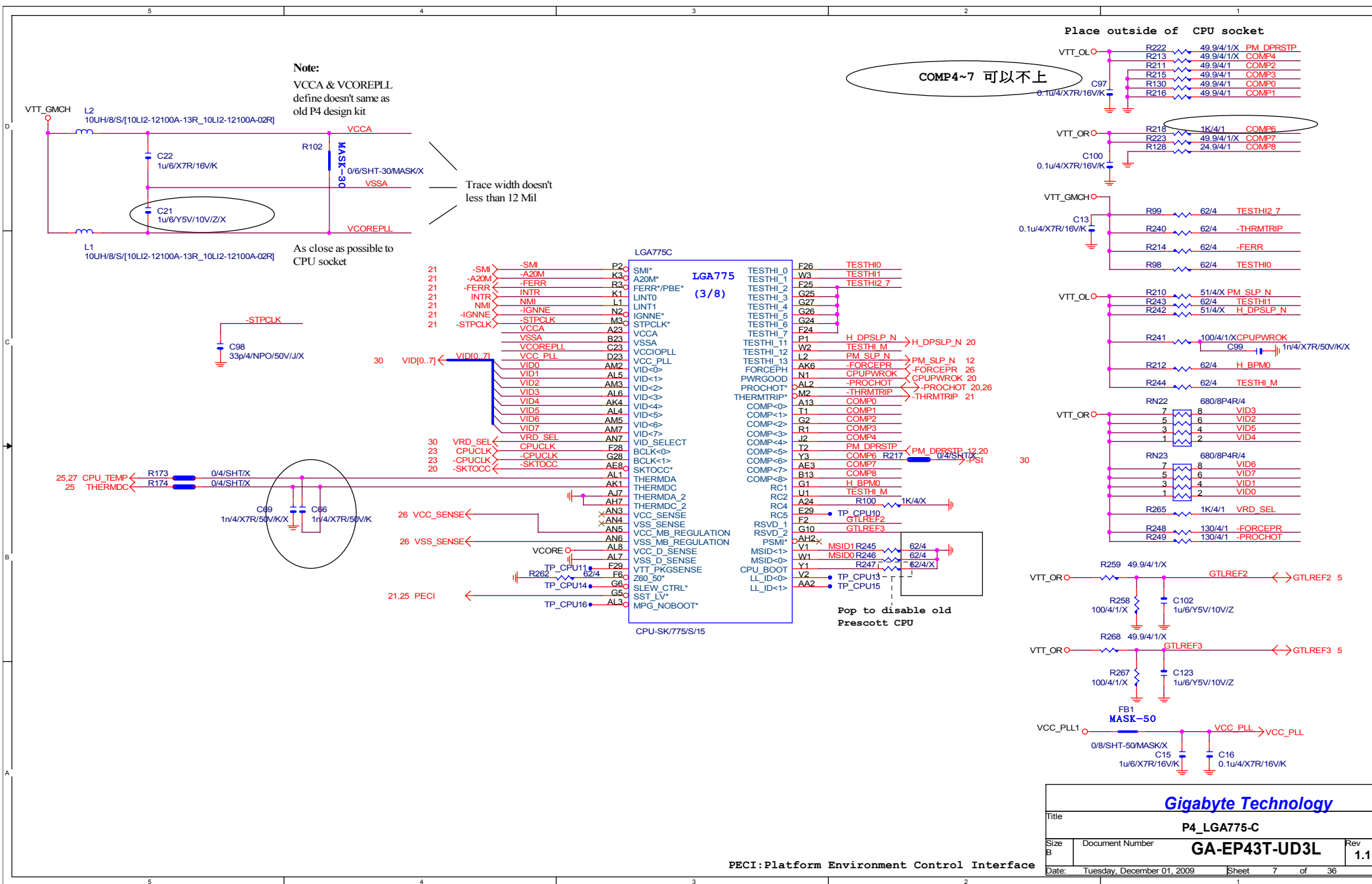
VTT OR  R264  62/4 -CPURST

RN19      62/8P4R/4

CR  
CPU RETENTION/X

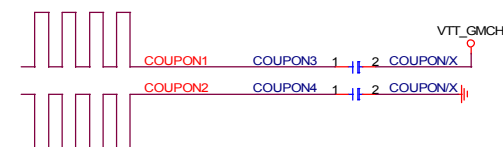


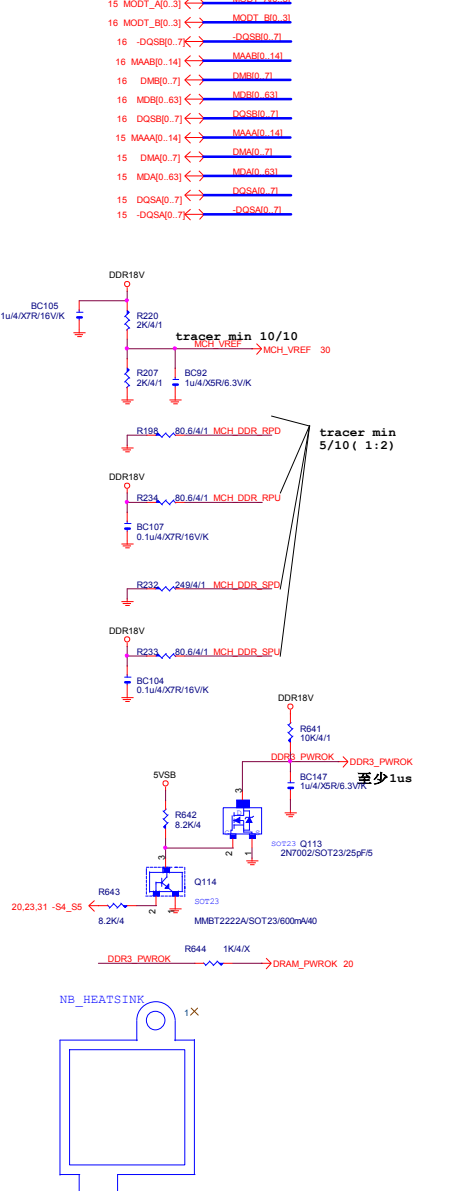
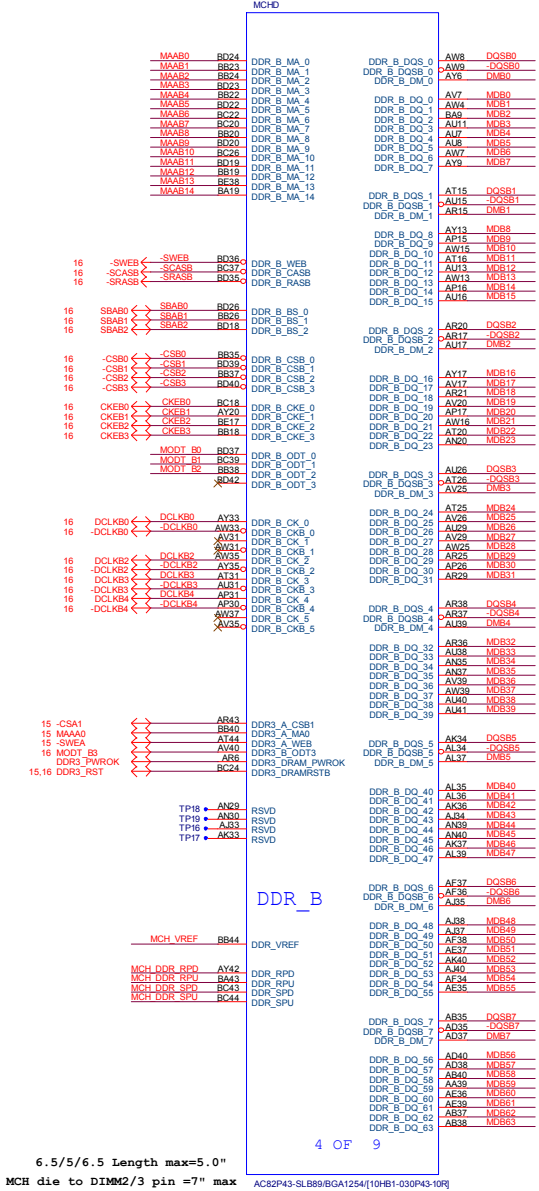
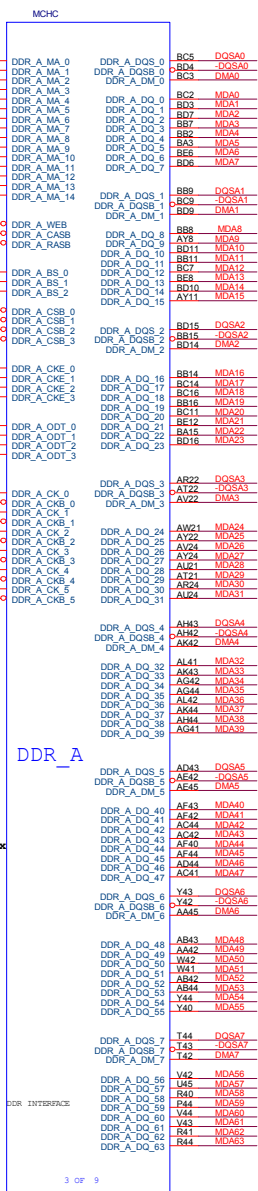
	FSA	FSB	FSC		
	FSBSEL0	FSBSEL1	FSBSEL2	Clock	
?	1	0	1	100MHz	
?	1	0	0	133MHz	3/4 400/533
G33	0	1	0	200MHz	2/2.66/3.33/4+ 400/533/667/800
G33	0	0	0	266MHz	2/2.5/3/4~ 533/667/800/1066
G33	0	0	1	333MHz	2/2.4/3.2/4# 667/800/1066/1333
	0	1	1	400MHz	





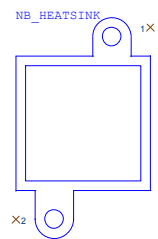






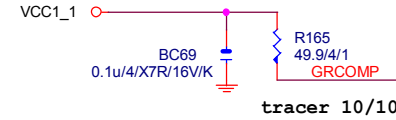
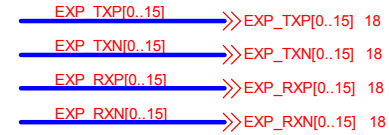
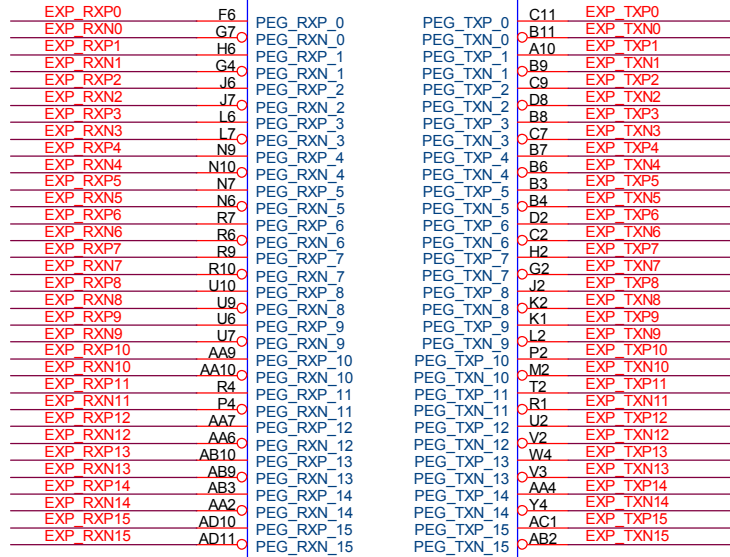
6.5/5/6.5 Length max=5.0"  
MCH die to DIMM0/1 pin =6" max  
FOR channel A

6.5/5/6.5 Length max=5.0"  
MCH die to DIMM2/3 pin =7" max  
FOR channel B

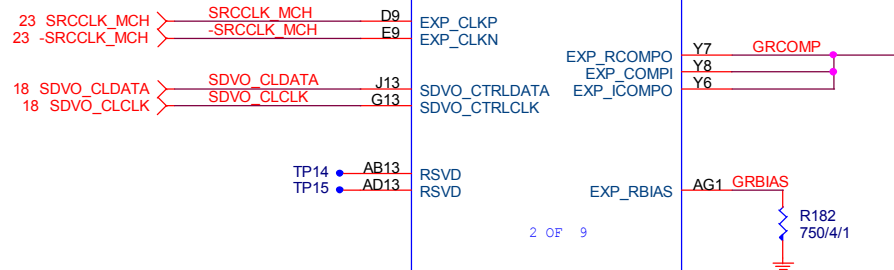
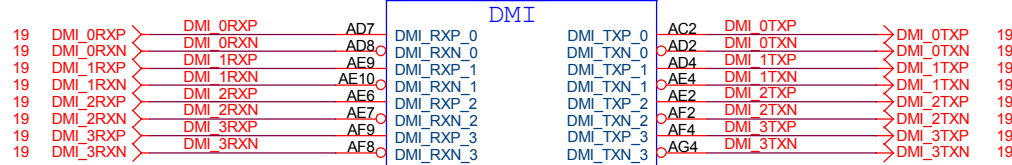


PCIEX16:16/5/5/5/16(breakout min 8/4/5/4/8)MCHB

Impedance=85 +- 17.5%



DMI:12/4/8/4/12  
Impedance=95 +- 17.5%



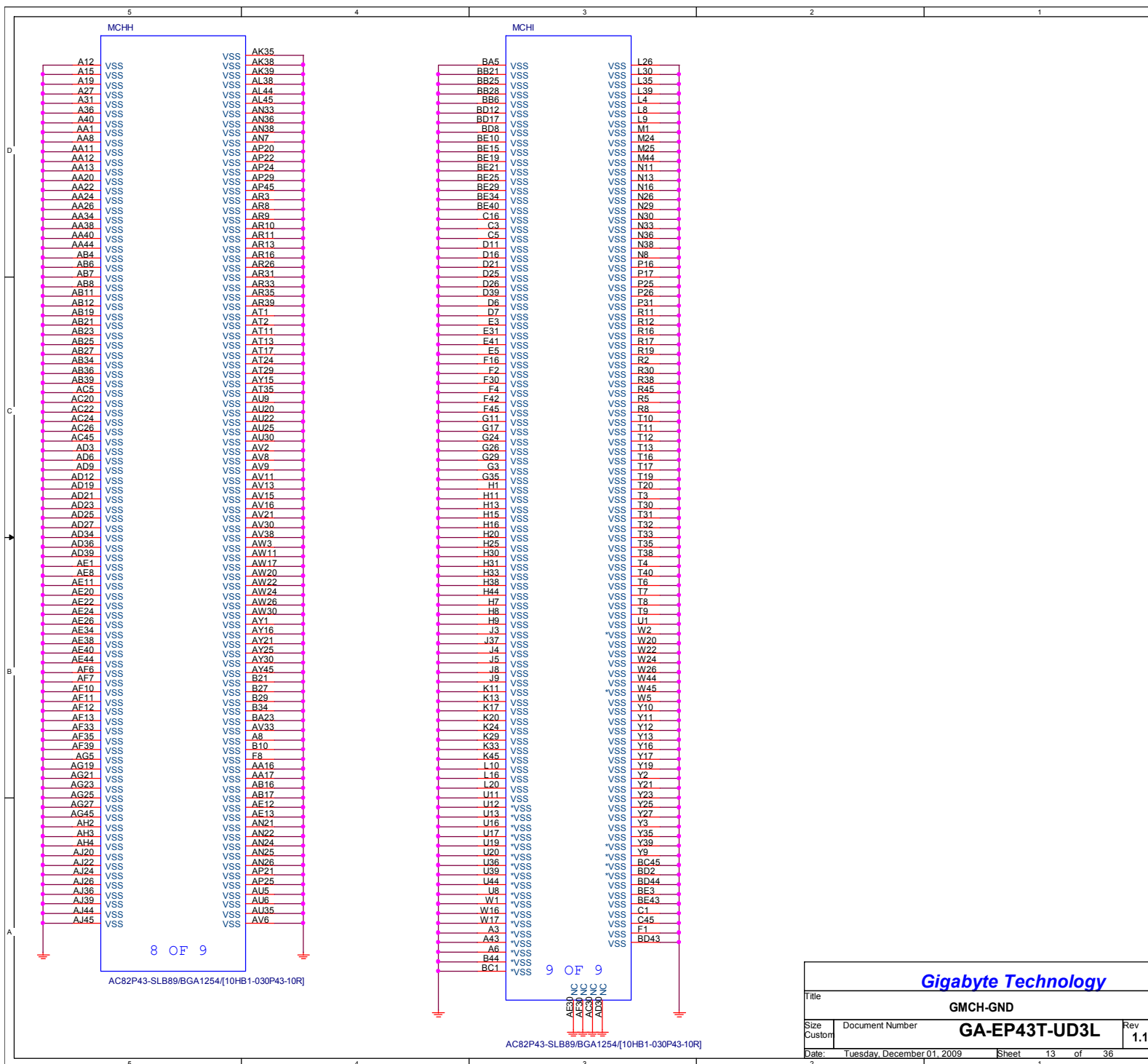
AC82P43-SLB89/BGA1254/[10HB1-030P43-10R]

2 OF 9

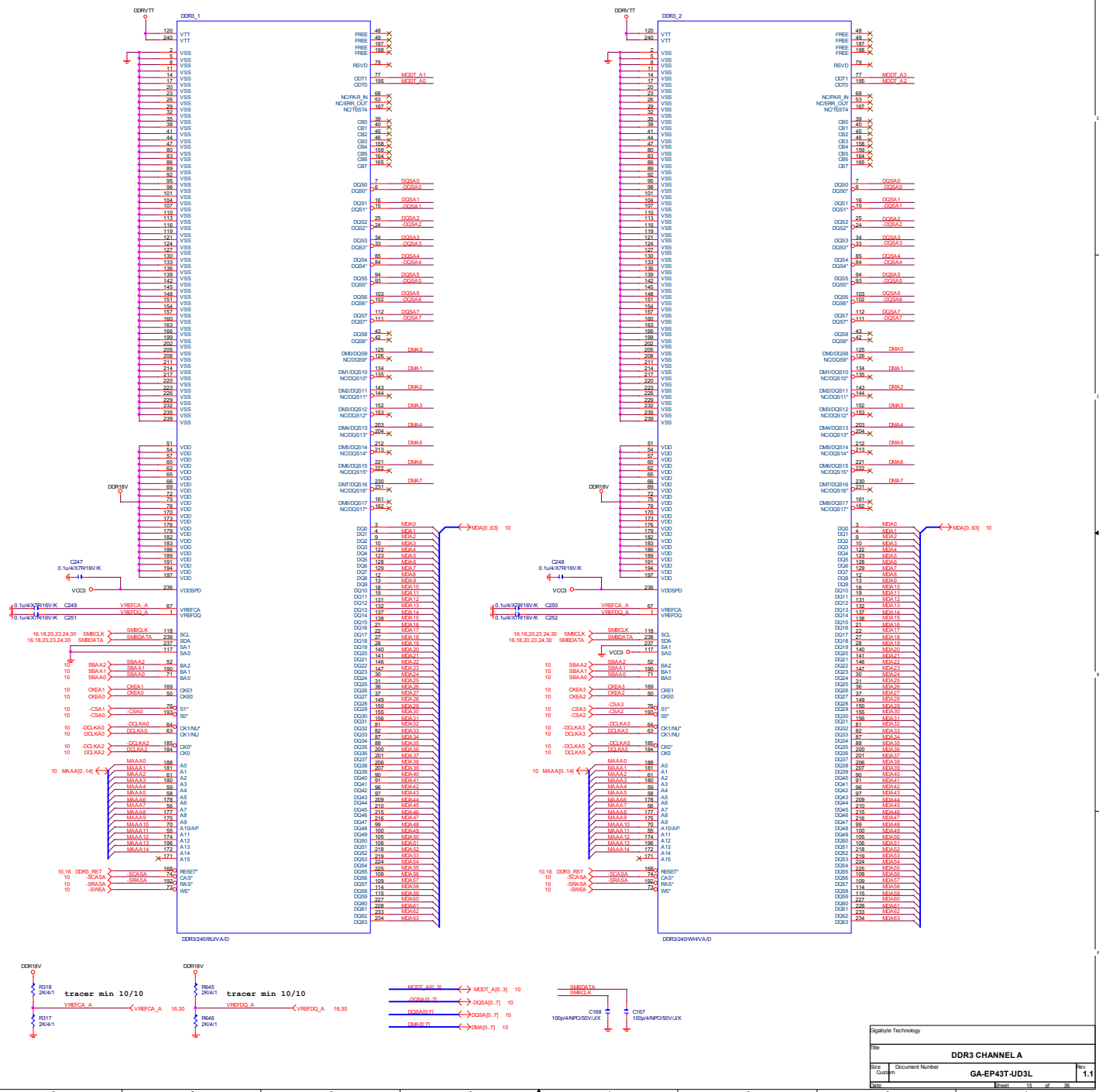
Gigabyte Technology

Title			GMCH-PCI E & DMI	
Size	Document Number	GA-EP43T-UD3L		Rev
Custom				1.1
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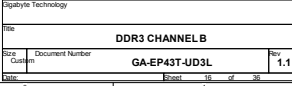








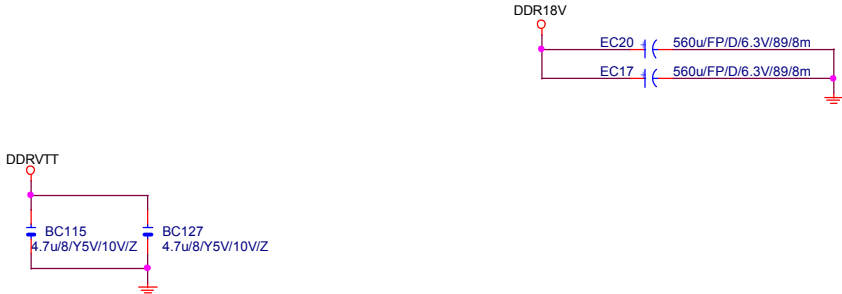






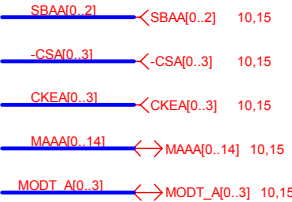
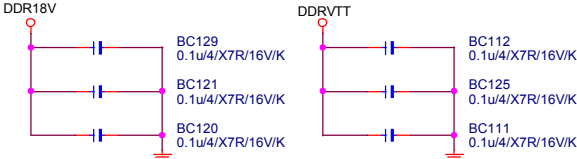
DDR TERMINATION  
CHANNEL A

DDRVTT Decouple



DDR18V Decouple

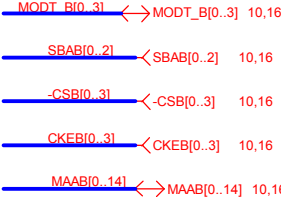
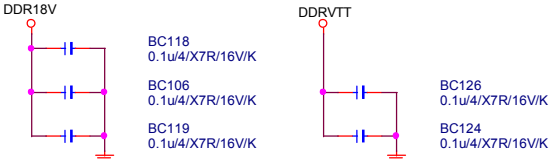
DDRVTT Decouple



DDR TERMINATION  
CHANNEL B

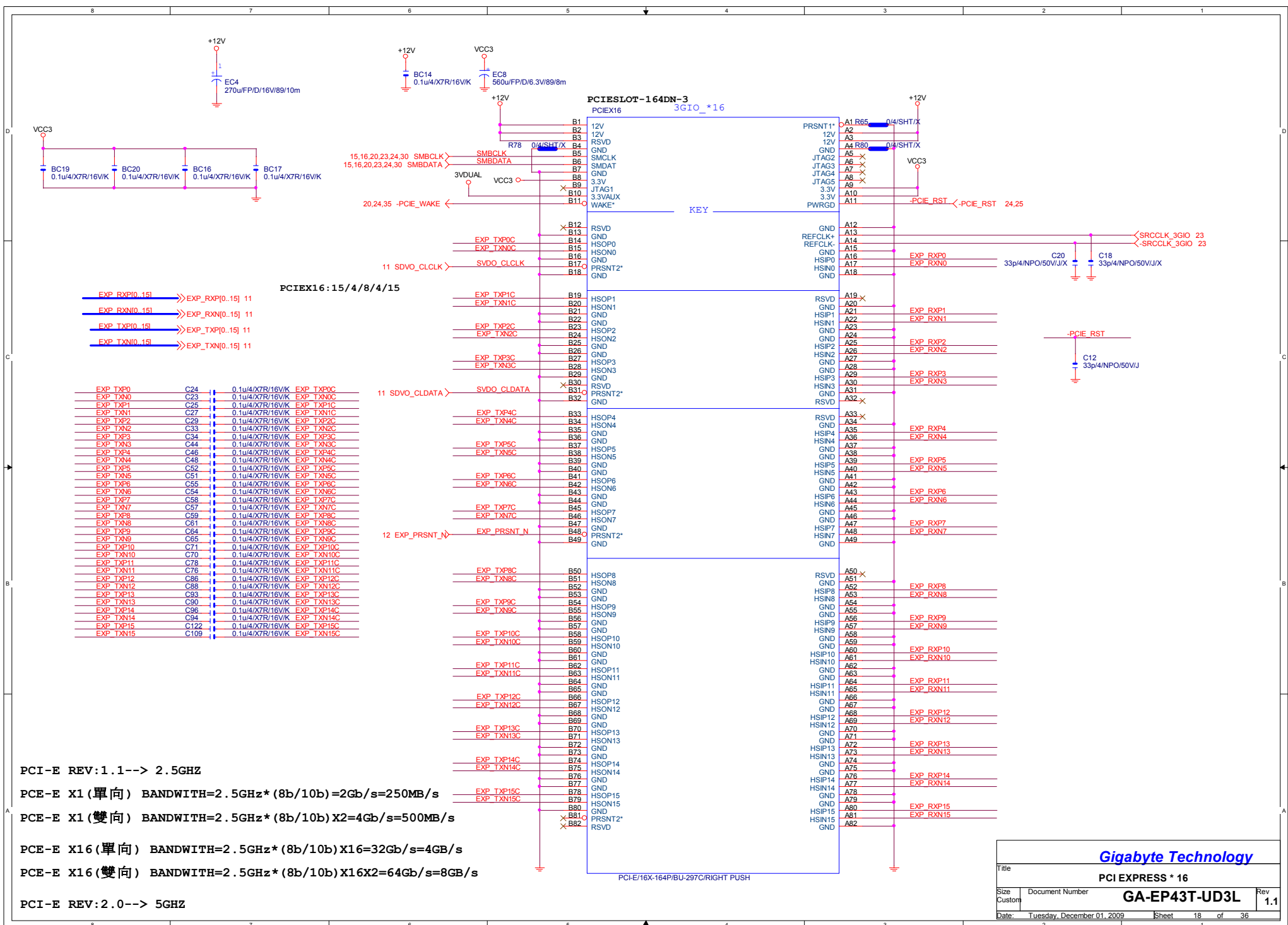
DDR18V Decouple

DDRVTT Decouple



Gigabyte Technology

Title			DDR3 TERMINATOR
Size	Document Number	GA-EP43T-UD3L	
Custom		Rev	1.1
Date:	Tuesday, December 01, 2009	Sheet	17 of 36



C124  
10p/4/NPO/50V/J/X

ICH33

C145  
100p/4/NPO/50V/J/X

24,36 -PCIRST

24 -PCIPME

24 -SERR

24 -STOP

24 -PLOCK

24 -TRDY

24 -PERR

24 -FRAME

24 -REQ0

24 -REQ1

24 -REQ2

24 -REQ3

24 -PIRQA

24 -PIROB

24 -PIROC

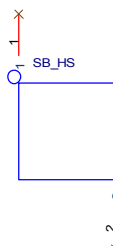
24 -PIROD

24 -PIROE

24 -PIROF

24 -PIROG

24 -PIROH



SB\_HS[12SP2-030030-51R\_12SP2-030030-52R\_12SP2-030030-53R]

FOR DDR3専用SB heat sink

ICHA

ICH9  
(1/6)  
PCI

AF82801JB-A0/BGA676/[10HB1-038280-G0R]

C10 A\_D0  
C8 A\_D1  
E9 A\_D2  
C9 A\_D3  
A5 A\_D4  
AD\_5  
E12 A\_D5  
E10 A\_D6  
B7 A\_D7  
B6 A\_D8  
B4 A\_D9  
E7 A\_D10  
A4 A\_D11  
H12 A\_D12  
C5 A\_D13  
D2 A\_D15  
E5 A\_D16  
G7 A\_D17  
E11 A\_D18  
G10 A\_D19  
G6 A\_D20  
D3 A\_D21  
H6 A\_D22  
G5 A\_D23  
C1 A\_D24  
C2 A\_D25  
C3 A\_D26  
D1 A\_D27  
J7 A\_D28  
F3 A\_D29  
G1 A\_D30  
H3 A\_D31

PCIE X1 :15/4/8/4/15  
Impedance=95 +- 17.5%

E11 -C\_BE0  
G9 -C\_BE1  
C4 -C\_BE2  
E8 -C\_BE3

DMI:12/4/8/4/12  
Impedance=95 +- 17.5%

11 DMI\_0TXN  
11 DMI\_0TXP  
11 DMI\_0RXN  
11 DMI\_0RXP  
11 DMI\_1TXN  
11 DMI\_1TXP  
11 DMI\_1RXN  
11 DMI\_1RXP  
11 DMI\_2TXN  
11 DMI\_2TXP  
11 DMI\_2RXN  
11 DMI\_2RXP  
11 DMI\_3TXN  
11 DMI\_3TXP  
11 DMI\_3RXN  
11 DMI\_3RXP

35 ML\_IN  
35 ML\_IP  
35 ML\_ON  
35 ML\_OP  
24 PCIE\_IN0  
24 PCIE\_IP0  
24 PCIE\_ON0  
24 PCIE\_OP0  
24 PCIE\_IN1  
24 PCIE\_IP1  
24 PCIE\_ON1  
24 PCIE\_OP1  
24 PCIE\_IN2  
24 PCIE\_IP2  
24 PCIE\_ON2  
24 PCIE\_OP2  
24 PCIE\_IN3  
24 PCIE\_IP3  
24 PCIE\_ON3  
24 PCIE\_OP3  
33 MIDE\_IN  
33 MIDE\_IP  
33 MIDE\_ON  
33 MIDE\_OP

VCC1\_5  
24,9/4/1  
tracer 4/8  
23 -SRCLK\_ICH  
23 SRCLK\_ICH

ICHB

ICH9  
(2/6)  
DMI

PCI-E

AF82801JB-A0/BGA676/[10HB1-038280-G0R]

USB:15/4.5/7.5/4.5/15  
Impedance=90+- 15%

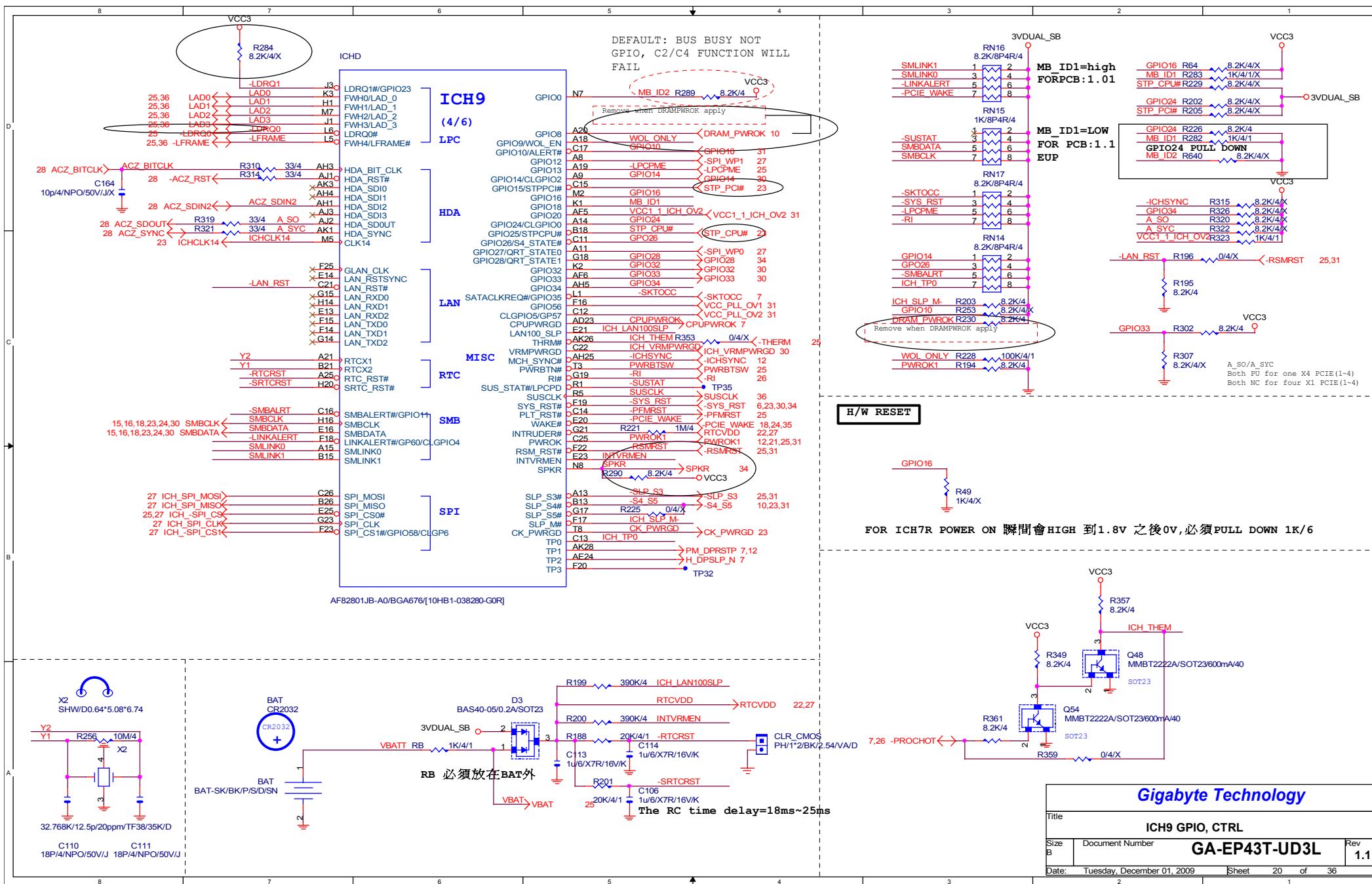
11 DMI\_0TXN  
11 DMI\_0TXP  
11 DMI\_0RXN  
11 DMI\_0RXP  
11 DMI\_1TXN  
11 DMI\_1TXP  
11 DMI\_1RXN  
11 DMI\_1RXP  
11 DMI\_2TXN  
11 DMI\_2TXP  
11 DMI\_2RXN  
11 DMI\_2RXP  
11 DMI\_3TXN  
11 DMI\_3TXP  
11 DMI\_3RXN  
11 DMI\_3RXP

PER6N\_GLAN\_RXN  
PER6N\_GLAN\_RXP  
PER6N\_GLAN\_TXN  
PER6N\_GLAN\_TXP  
PET1N  
PET1P  
PER2N  
PER2P  
PER3N  
PER3P  
PER4N  
PER4P  
PER5N  
PER5P  
PET5N  
PET5P

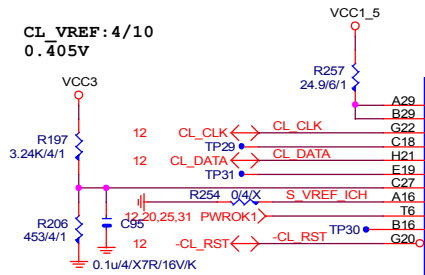
USBRBIASN  
USBRBIASP  
CLK48  
10p/4/NPO/50V/J/X

Gigabyte Technology

Title			ICH9-PCI, DMI, LAN, USB
Size B	Document Number	GA-EP43T-UD3L	
		Rev	1.1
Date: Tuesday, December 01, 2009		Sheet	19 of 36



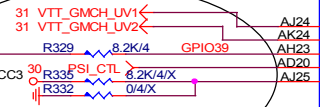
CL\_VREF:4/10  
0.405V



31 ICH\_FAN\_PWM0< ICH\_FAN\_PWM0 AJ21  
31 ICH\_FAN\_PWM1< ICH\_FAN\_PWM1 AJ22  
31 ICH\_FAN\_PWM2< ICH\_FAN\_PWM2 AK22

31 ICH\_FAN\_TACH0< ICH\_FAN\_TACH0 AH21  
31 ICH\_FAN\_TACH1< ICH\_FAN\_TACH1 AK21  
31 ICH\_FAN\_TACH2< ICH\_FAN\_TACH2 AH22  
31 ICH\_FAN\_TACH3< ICH\_FAN\_TACH3 AK23

25 SST\_IO< R255 0/4/SHT/X C19



GPIO49/DMI Termination Voltage  
:LOW FOR DESKTOP

GPIO48 : Lo For Media BIOS  
Hi For Disty BIOS

ICH9  
(3/6)

SATA

HOST

AF82801JB-A0/BGA676/[10HB1-038280-GOR]

SATA0RXN< AK17 SATA0RXN  
SATA0RXP< AJ17 SATA0RXP  
SATA0TXN< AK19 SATA0TXN  
SATA0TXP< AJ19 SATA0TXP  
SATA1RXN< AK15 SATA1RXN  
SATA1RXP< AJ15 SATA1RXP  
SATA1TXN< AH16 SATA1TXN  
SATA1TXP< AF16 SATA1TXP  
SATA2RXN< AJ13 SATA2RXN  
SATA2RXP< AK13 SATA2RXP  
SATA2TXN< AH14 SATA2TXN  
SATA2TXP< AF14 SATA2TXP  
SATA3RXN< AK11 SATA3RXN  
SATA3RXP< AJ11 SATA3RXP  
SATA3TXN< AF12 SATA3TXN  
SATA3TXP< AH12 SATA3TXP  
SATA4RXN< AJ9 SATA4RXN  
SATA4RXP< AK9 SATA4RXP  
SATA4TXN< AF10 SATA4TXN  
SATA4TXP< AH9 SATA4TXP  
SATA5RXN< AK7 SATA5RXN  
SATA5RXP< AF8 SATA5RXP  
SATA5TXN< AH7 SATA5TXP  
SATA5TXP< AF18 -SRCCLK SATA -SRCCLK SATA 23  
SATA5TXP< AF19 SRCCLK SATA SRCCLK SATA 23

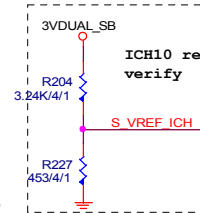
SATALED#< AE7 -SATALED -SATALED 34  
SATABIASN< AK6 R327 24.9/4/1  
SATABIASP< AJ6

SATARBIAASN=4MIL  
AK25 VTT\_GMCH\_OV1 31  
AE20 VTT\_GMCH\_OV2 31  
AE22 ICH\_GPIO37 VCC1\_1\_ICH\_OV1 31  
AE22 ICH\_GPIO37 ICH\_GPIO37 30  
AD21 SATA4GP  
AD21 SATA5GP

A20GATE< P8 A20GATE A20GATE 25  
A20M#< AJ28 -A20M -A20M 7  
IGNNE#< AC22 -IGNNE -IGNNE 7  
INIT3\_3V#< M3 -FWHINIT -FWHINIT 7  
INIT#< AE23 -HINIT -HINIT 5  
INTR< AH27 INTR INTR 7  
FERR#< AJ27 -FERR -FERR 7  
NMI< AF24 NMI NMI 7  
RCIN#< L3 -KBRST -KBRST 25  
SERIRQ< N8 SERIRQ SERIRQ 25,36  
SMI#< AH26 -SMI -SMI 7  
STPCLK#< AJ29 -STPCLK -STPCLK 7  
THRMTRIP#< AD24 -THRMTRIP -THRMTRIP 7  
PECI< AC23 SB\_PECI SB\_PECI 7,25

ICH9R

SATA:15/4/8/4/15  
Impedance=95 +- 17.5%



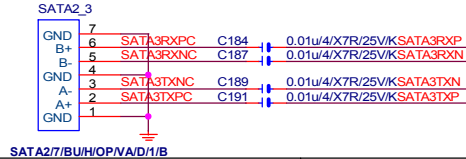
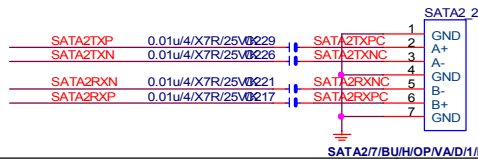
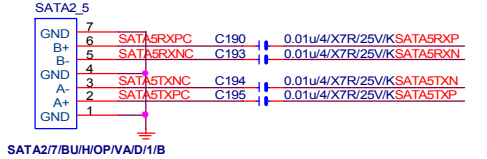
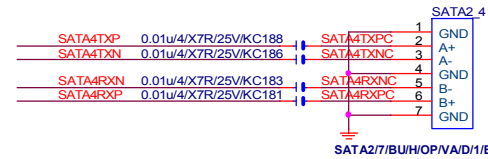
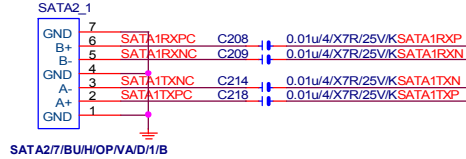
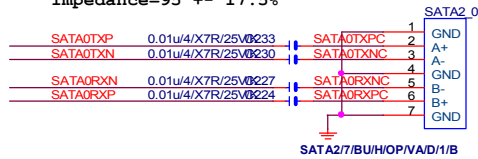
Can't SWAP PIN

ICH10 remove after  
verify

The ICH8 integrated GbE LAN test mode is activated any time the ICH8 GPIO39 signal is not at a low logic level.

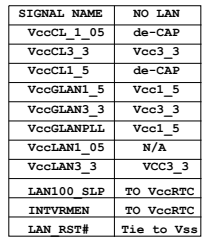
Workaround  
Under investigation. Possible workaround is to use a weak pulldown resistor on GPIO39 to ensure signal is always low

SATA:15/4/8/4/15  
Impedance=95 +- 17.5%



Gigabyte Technology

Title			
ICH9- SATA, FAN CTRL			
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<b>Gigabyte Technology</b>			
Title			
<b>ICH9-PWR &amp; GND</b>			
Size C	Document Number	<b>GA-EP43T-UD3L</b>	Rev 1.1
Date:	Tuesday, December 01, 2009	Sheet	22 of 36

# CLK GEN CK505

50歐姆 : [18/4/10/4/18]

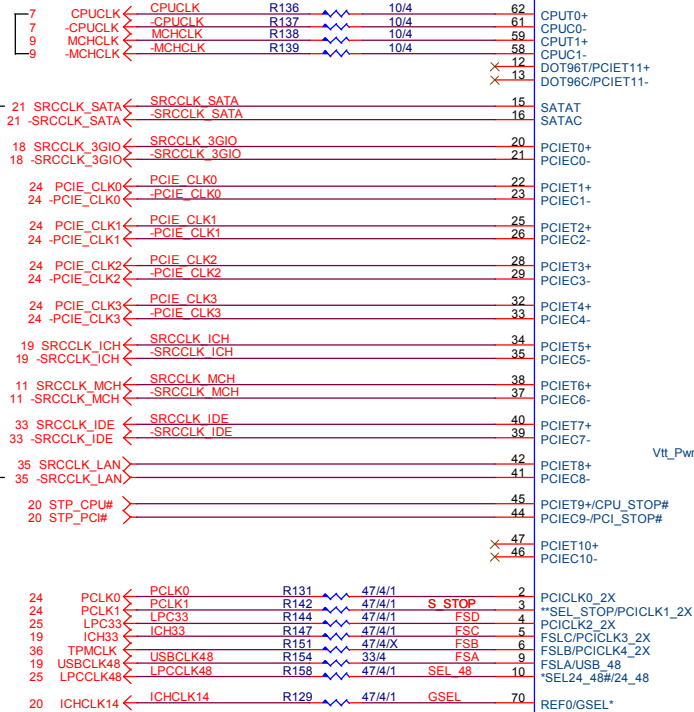
50歐姆 : [18/4/10/4/18]

50歐姆 : [18/4/10/4/18]

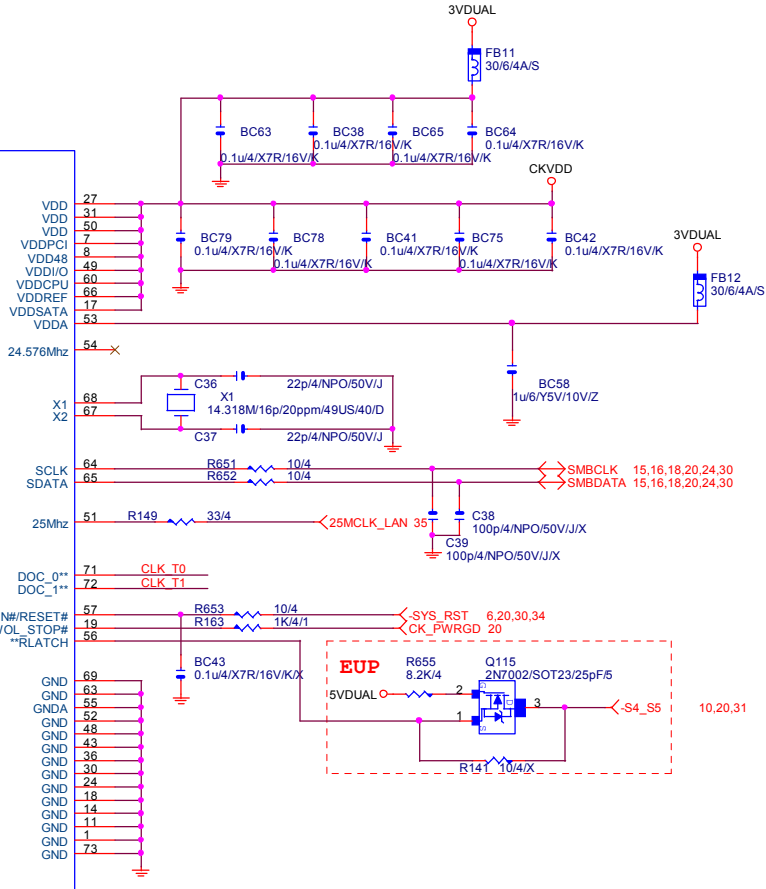
50歐姆 : [4/10]

50歐姆 : [4/10]

50歐姆 : [4/10]



ICS9LPRS914[10HL6-180914-20R]

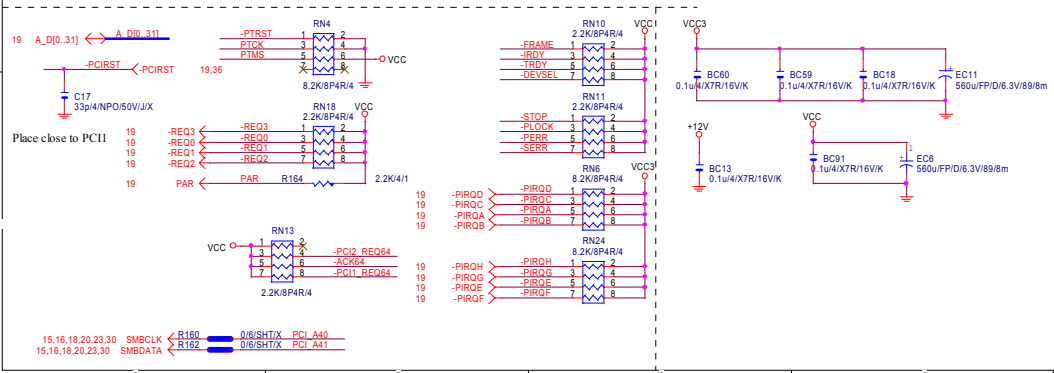
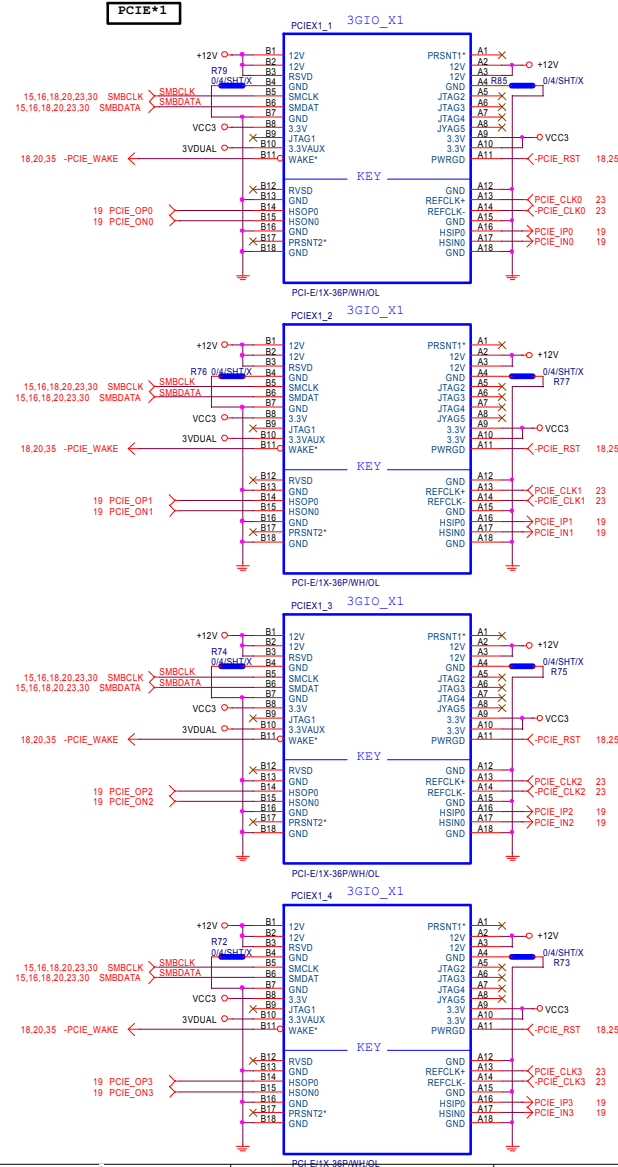
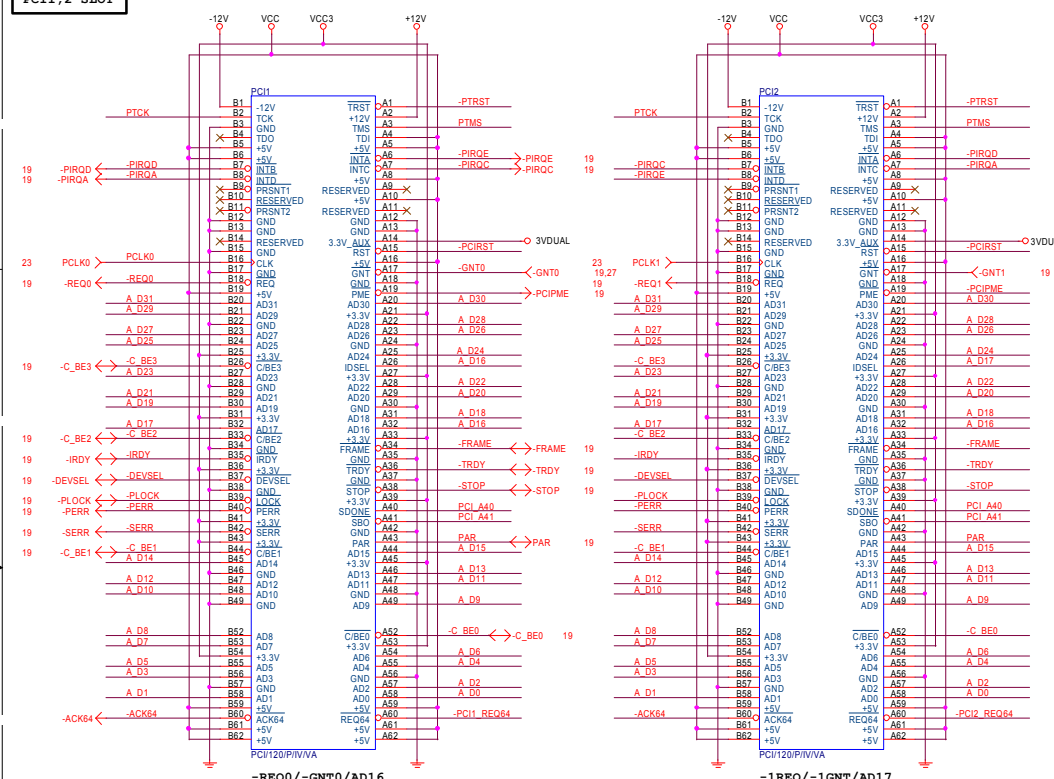


GSEL=1, 96Mhz from 14/15, SATACLK from 17/18  
GSEL=0, SATACLK from 14/15, PCIECLK from 17/18

Gigabyte Technology

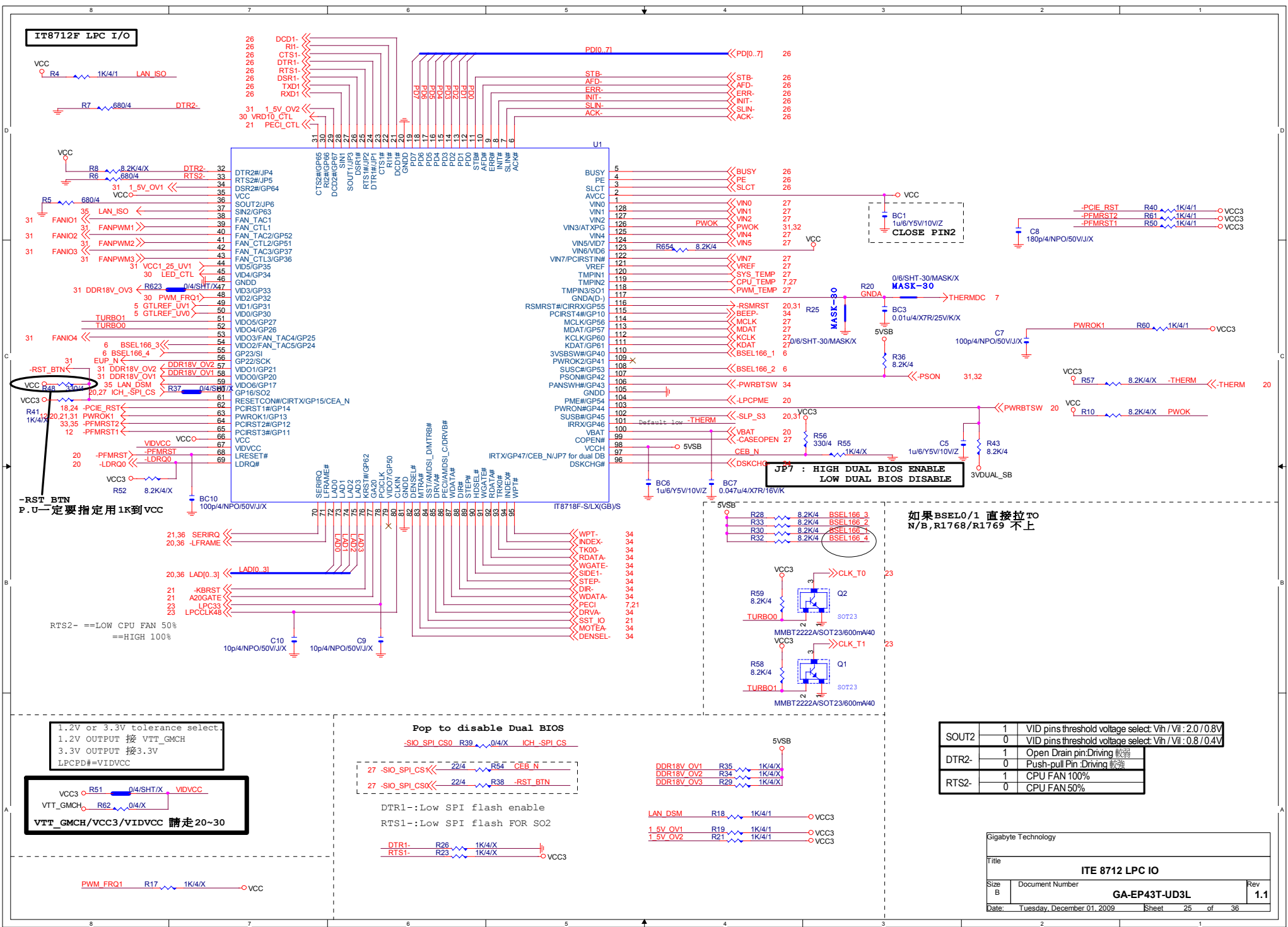
Title			CK505 CLK GEN
Size	Custom	Document Number	GA-EP43T-UD3L
Date:	Tuesday, December 01, 2009	Sheet	23 of 36
			Rev 1.1







# IT8712F LPC I/O



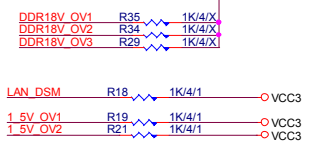
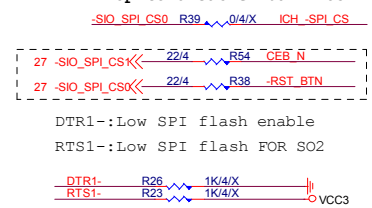
-RST\_BTN  
P.U一定要指定用1K到VCC

1.2V or 3.3V tolerance select.  
1.2V OUTPUT 接 VTT\_GMCH  
3.3V OUTPUT 接3.3V  
LPCPD# = VIDVCC

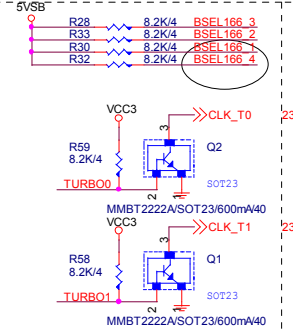
VCC3 R51 0.4/SHT/X VIDVCC  
VTT\_GMCH R62 0.4/X  
VTT\_GMCH/VCC3/VIDVCC 請走20~30

PWM\_FRQ1 R17 1K4/X VCC

## Pop to disable Dual BIOS

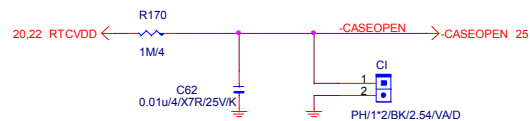
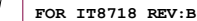


如果BSEL0/1 直接拉到N/B, R1768/R1769 不上



SOUT2	1	VID pins threshold voltage select: Vin /Vil: 2.0 /0.8V
DTR2-	0	VID pins threshold voltage select: Vin /Vil: 0.8 /0.4V
RTS2-	1	Open Drain pin: Driving 軟體
	0	Push-pull Pin: Driving 軟體
	1	CPU FAN 100%
	0	CPU FAN 50%



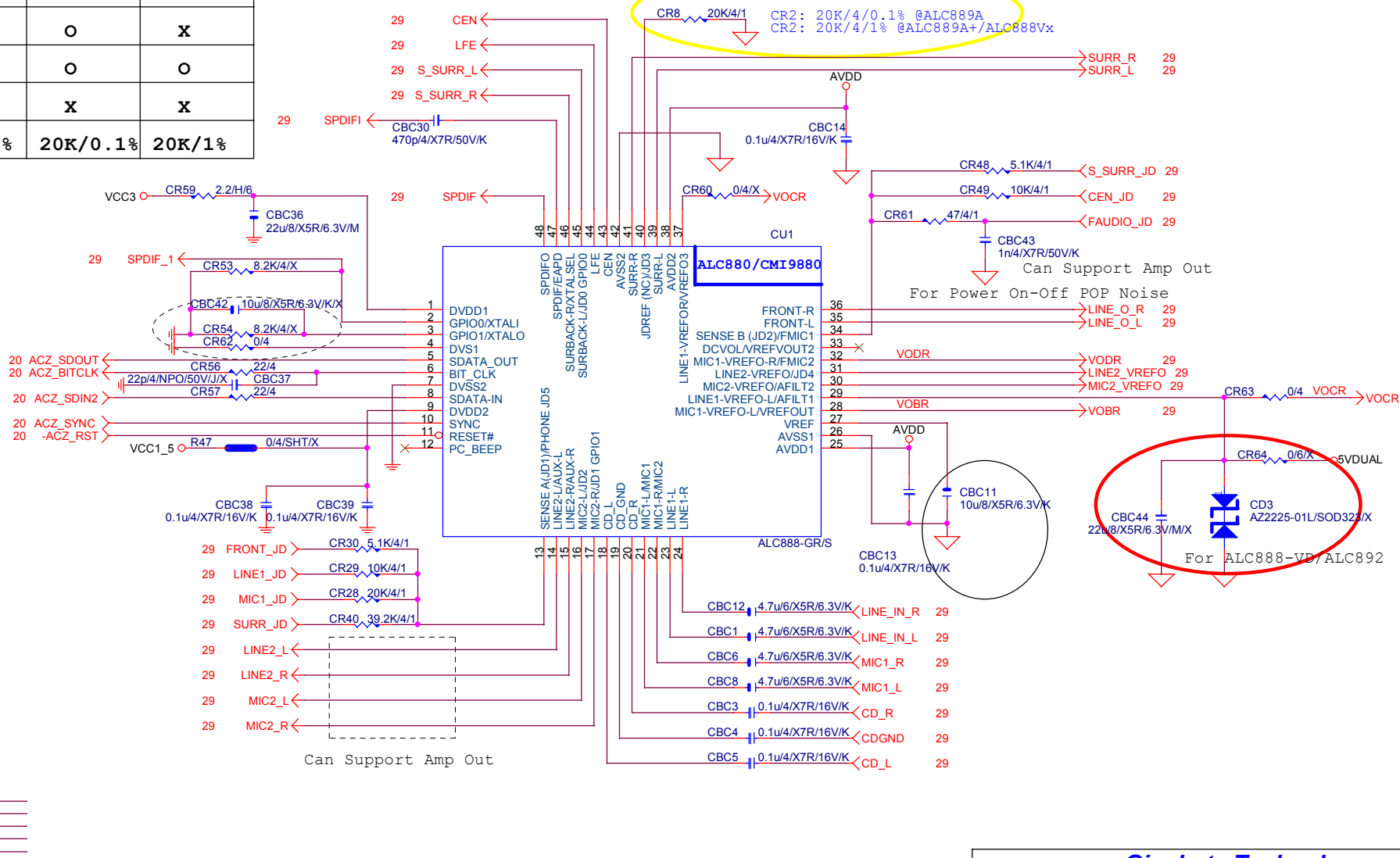


## Case Open Circuits

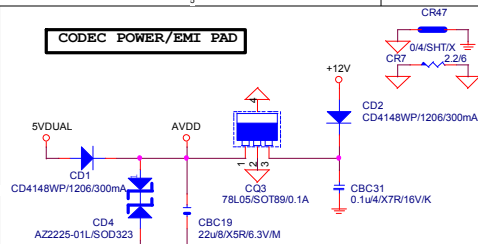


ALC889A+/ALC889A/ALC888Vx Colay

	ALC889A+	ALC889A	ALC888Vx
CR107	X	O	X
CR108	X	O	X
CR109	X	O	O
CR110	O	X	X
CR2	20K/1%	20K/0.1%	20K/1%

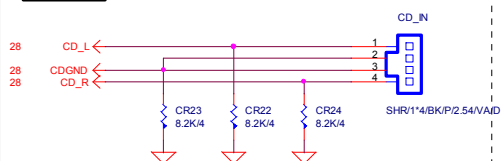


## CODEC POWER/EMI PAD

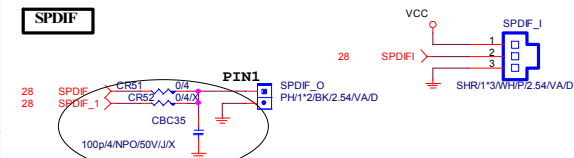


## CO-LAYOUT

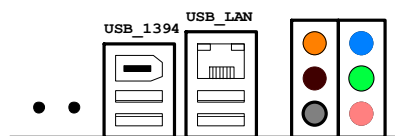
## CD IN



## SPDIF

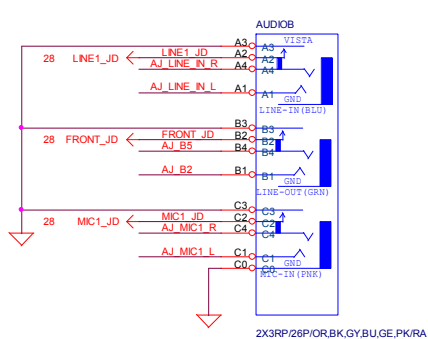
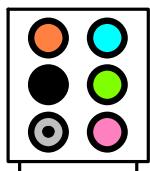


## AZALIA JACK

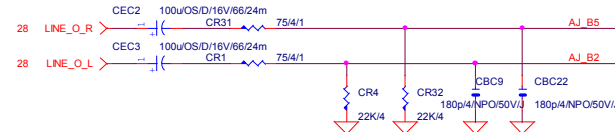


3P/26P/OR,BK,GY,BU,GE,PK/RA/D/1/B  
VISTA規範: REAR-->BLK, CEN/SUB-->ORG

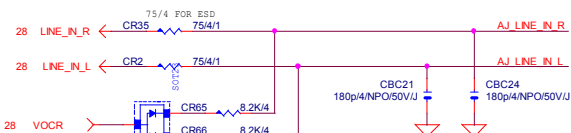
## BTX AZALIA CONNECTOR



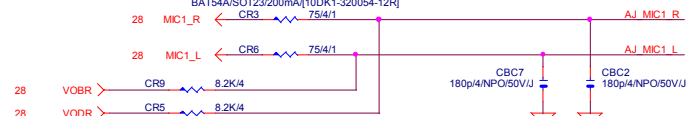
## LINE-OUT



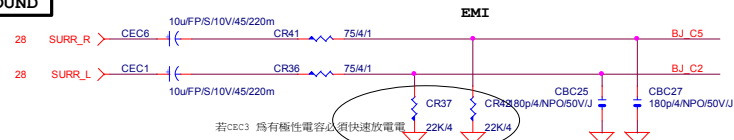
## LINE-IN



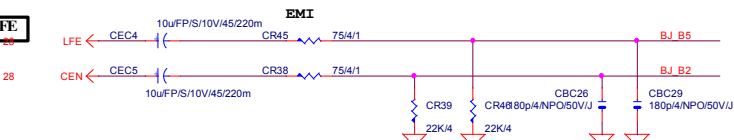
## MIC-IN



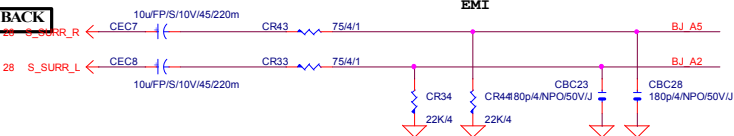
## SURROUND



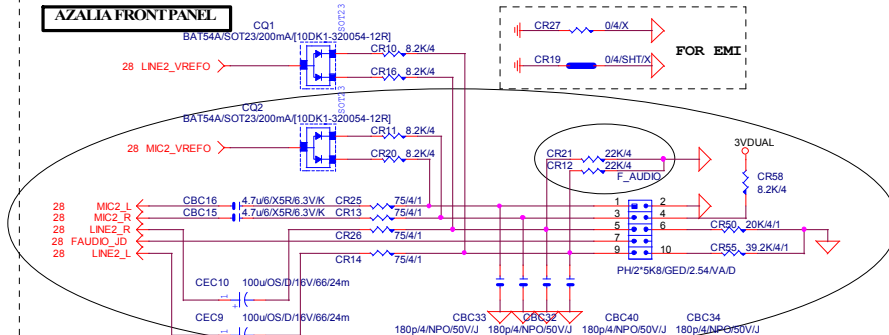
## CEN/LFE



## SURRBACK



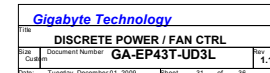
## AZALIA FRONT PANEL



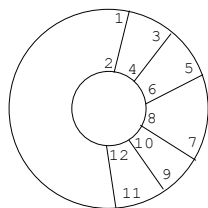
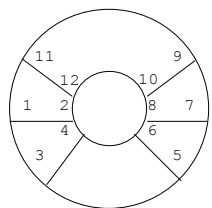
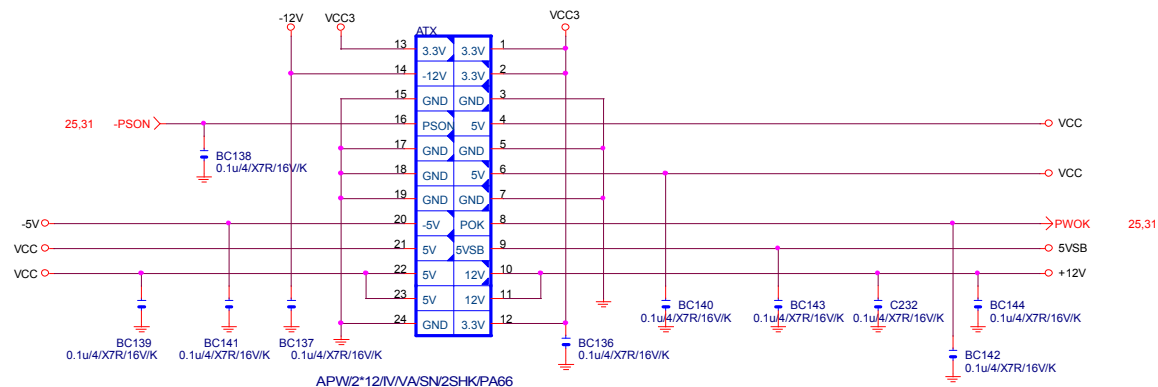
Gigabyte Technology

Title		AUDIO JACK	
Size		GA-EP43T-UD3L	
Custom		Rev 1.1	
Date		Tuesday, December 01, 2009	
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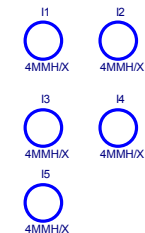
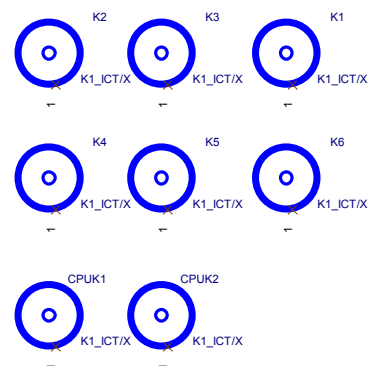
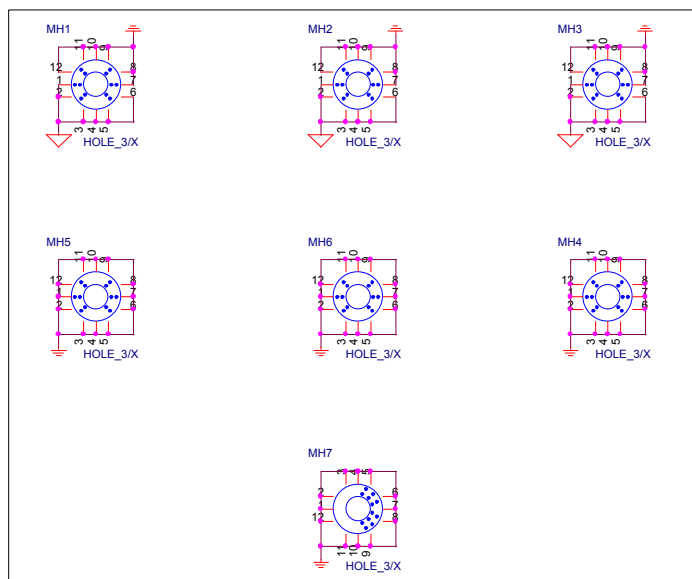




## ATX POWER CONNECTOR



螺絲孔位置圖 (注意Footprint不同)

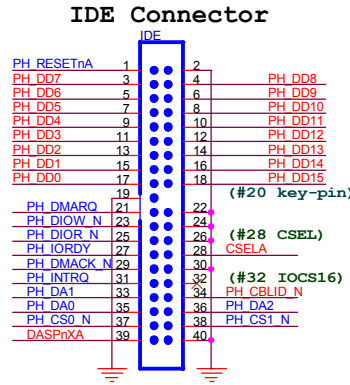
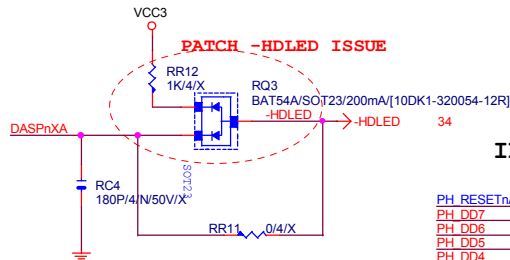
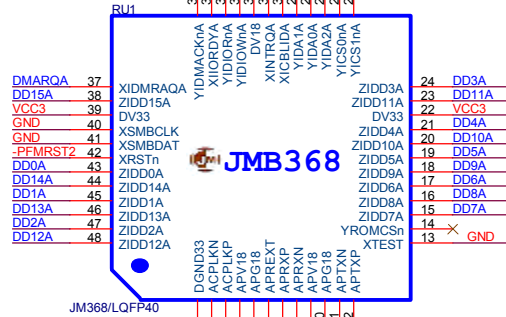
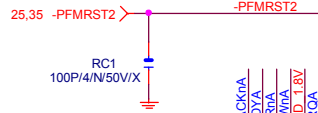
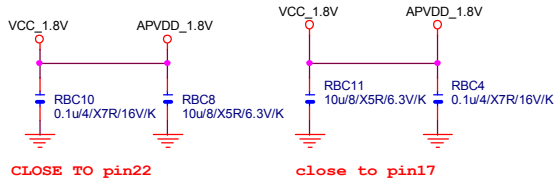
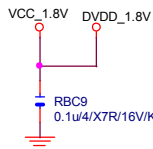
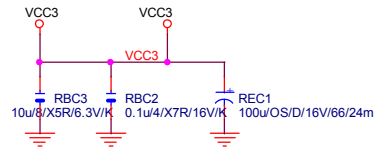
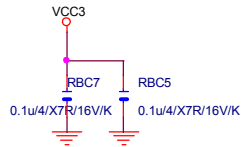
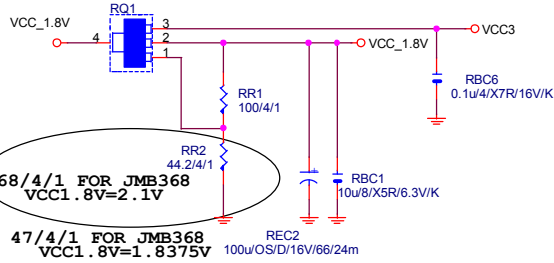


Gigabyte Technology			
Title			
ATX POWER CONNECTOR			
Size	Document Number	GA-EP43T-UD3L	
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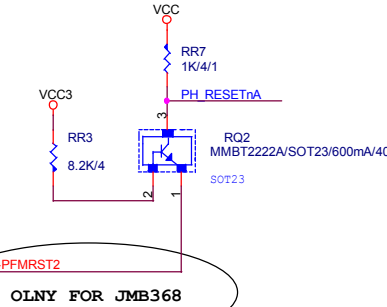


# 3.3V to 1.8V Voltage Regulator

L1117LG/N/SOT23/1A



BH2\*20K20/WH/SHN2.54/VA/PA46



PH DD7 DD7A  
PH DD8 DD8A  
PH DD6 DD6A  
PH DD9 DD9A

PH DD5 DD5A  
PH DD4 DD4A  
PH DD10 DD10A  
PH DD11 DD11A

PH DD3 DD3A  
PH DD12 DD12A  
PH DD2 DD2A  
PH DD13 DD13A

PH DD1 DD1A  
PH DD0 DD0A  
PH DD14 DD14A  
PH DD15 DD15A

PH DIOW\_N DIOWnA  
PH DIOR\_N DIORnA  
PH DMACK\_N DMACKnA  
PH DA1 DA1A  
PH DA0 DA0A  
PH CS0\_N CS0nA  
PH CS1\_N CS1nA

PH IORDY IORDYA  
PH DMARQ DMARQA  
PH INTRQ INTRQA  
PH CBLID\_N PDIAGnA

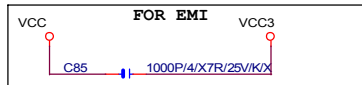
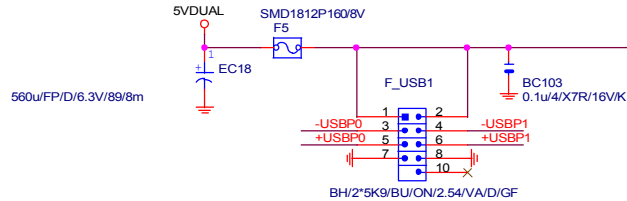
**Gigabyte Technology**

**JMR363**

Title: JMR363  
Size: Custom  
Document Number: GA-EP43T-UD3L  
Date: Tuesday, December 01, 2009  
Sheet: 33 of 36  
Rev: 1.1

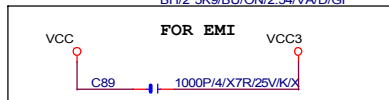
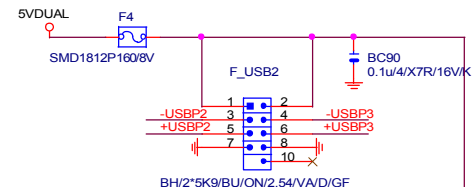
# FRONT USB1

19 +USBP0 <-> +USBP0  
19 -USBP0 <-> -USBP0  
19 +USBP1 <-> +USBP1  
19 -USBP1 <-> -USBP1

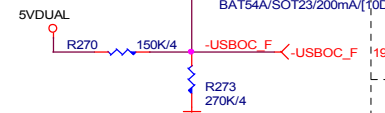


# FRONT USB2

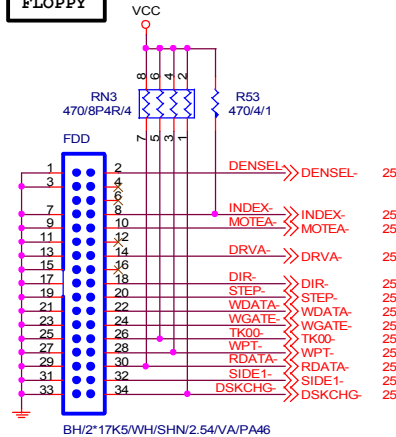
19 +USBP2 <-> +USBP2  
19 -USBP2 <-> -USBP2  
19 +USBP3 <-> +USBP3  
19 -USBP3 <-> -USBP3



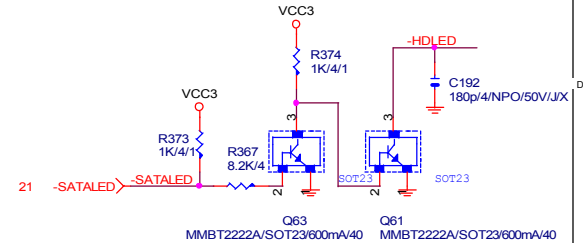
# FRONT USB OC



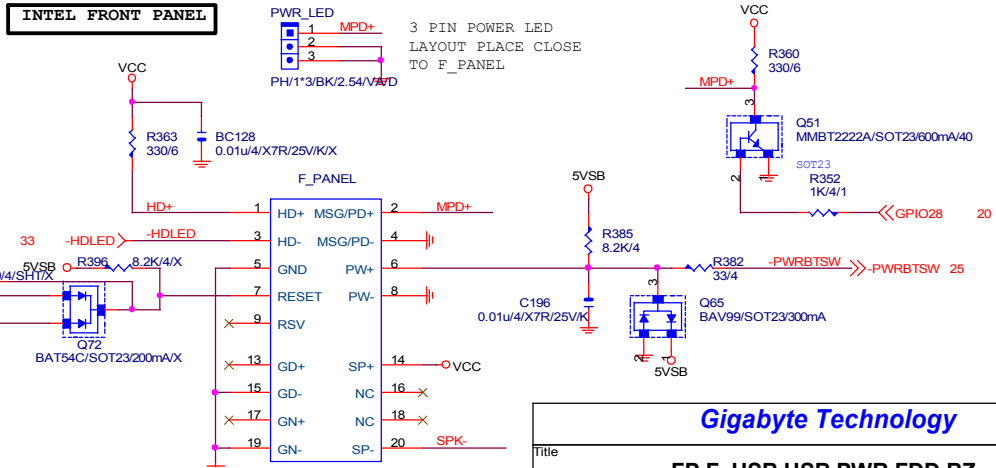
# FLOPPY



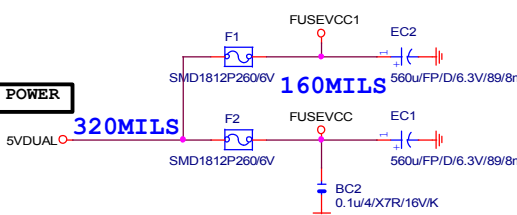
# SATA LED



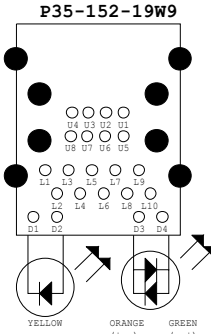
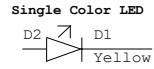
# INTEL FRONT PANEL



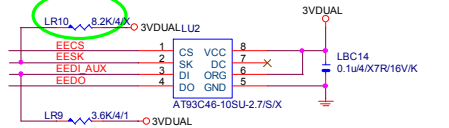
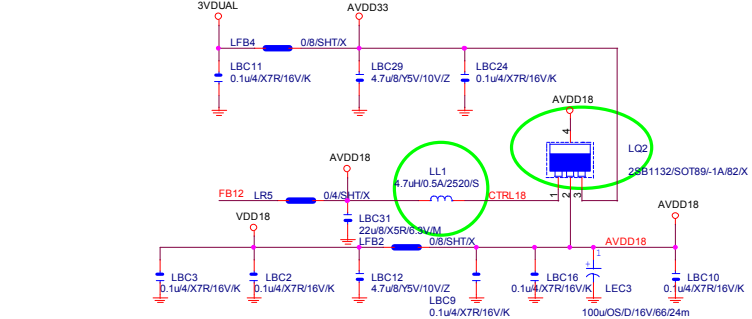
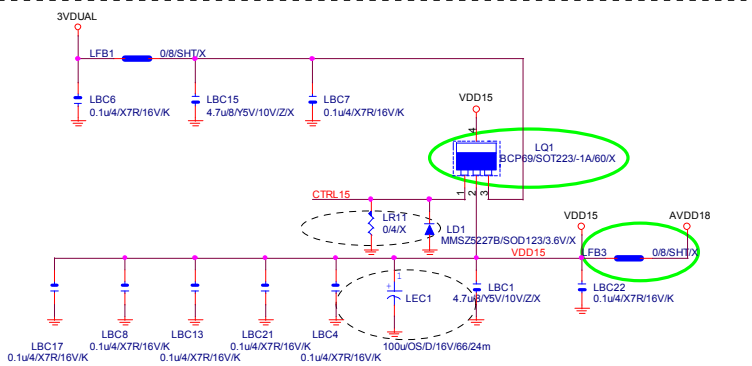
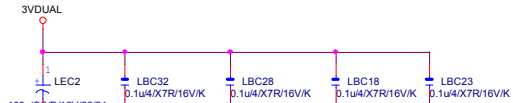
# USB POWER



Gigabyte Technology			
Title			
FP,F_USB,USB PWR,FDD,BZ			
Size	Document Number	Rev	
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	RTL8111B / RTL8101E	RTL8111C	
AVDD33	3.3V	3.3V	
AVDD18	1.8V	1.2V	
EVDD18	1.8V	1.2V	
DVDD15	1.5V	1.2V	



**USB LAN CONNECTOR**

LAN 100歐姆:[30/4/8/4/30] FOR B 製程

USB\_LAN

LBC27 100p4/NPO/50V/U/X

P\_M0D+ L1  
P\_M0D- L2  
P\_M0H+ L3  
P\_M0H- L4  
P\_M0L+ L5  
P\_M0L- L6  
P\_M0B+ L7  
P\_M0B- L8  
P\_M0G+ L9  
P\_M0G- L10

LBC34 100p4/NPO/50V/U

UP

DOWN

USB+LAN1G/GQ.YIOS/RA/D/8C  
8 Core

90歐姆:[15/4.5/7.5/4.5/15]

**USB POWER**

3VDUAL

LFB5 0/6/SHT/X

LBC33 0.1u4/X7R/16V/K

FUSEVCC1

LBC26 0.1u4/X7R/16V/K

D1 LED\_ACT\_TXRX  
D2 LED\_GREEN  
D3 LED\_LINK1000  
D4

VR1  
VR2  
VR3  
VR4  
VR5  
VR6  
VR7  
VR8

-USBP5  
+USBP5  
-USBP4  
+USBP4

19  
19  
19  
19

**USB LAN**

REAR USB OC

5VDUAL

R27 150K/4

R22 270K/4

D1 BAT54A/SOT23/200mA/[10DK1-320054-12R]

FUSEVCC1

-USBOC\_R  
+USBOC\_R 19

**Gigabyte Technology**

RTL8111C

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**USB\_LAN CONNECTOR**

LAN 100歐姆:[30/4/8/4/30] FOR B 製程

USB\_LAN

LBC27 100p4/NPO/50V/U/X

P\_M0D+ L1  
P\_M0D- L2  
P\_M0H+ L3  
P\_M0H- L4  
P\_M0L+ L5  
P\_M0L- L6  
P\_M0B+ L7  
P\_M0B- L8  
L9  
L10

LBC34 100p4/NPO/50V/U

UP

DOWN

USB+LAN1G/GQ.YIOS/RA/D/8C  
8 Core

90歐姆:[15/4.5/7.5/4.5/15]

**USB POWER**

3VDUAL

LFB5 0/6/SHT/X

LBC33 0.1u4/X7R/16V/K

FUSEVCC1

LBC26 0.1u4/X7R/16V/K

D1 LED\_ACT\_TXRX  
D2 LED\_GREEN  
D3 LED\_LINK1000  
D4

L17 150/6  
L19 150/6  
L18 150/6

U1 -USBP5  
U2 +USBP5 19  
U3 -USBP5  
U4 +USBP5 19  
U5 -USBP4  
U6 +USBP4 19  
U7 -USBP4  
U8 +USBP4 19

**USB\_LAN**

**REAR USB OC**

5VDUAL

R27 150K/4

R22 270K/4

D1 BAT54A/SOT23/200mA/[10DK1-320054-12R]

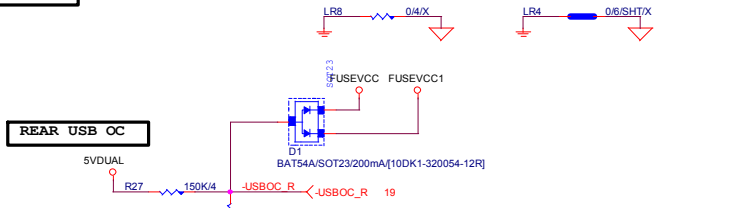
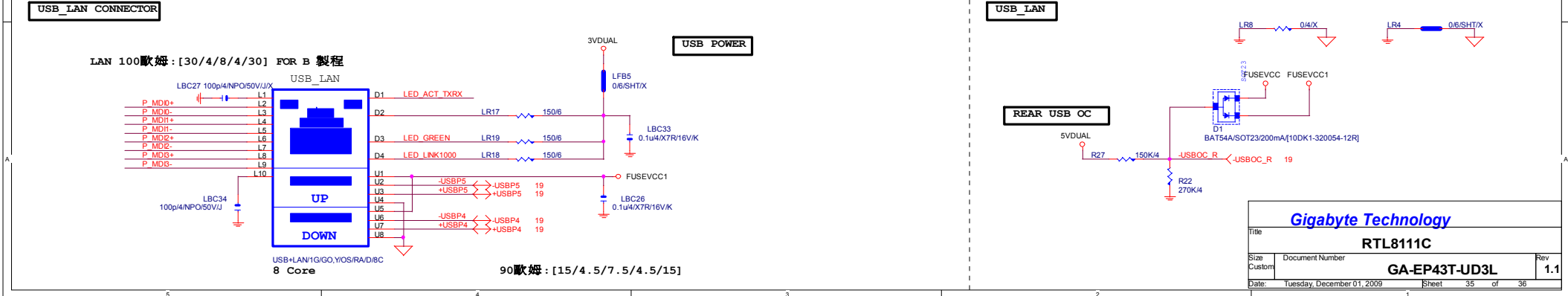
FUSEVCC1

USB\_OC\_R -USB\_OC\_R 19

**Gigabyte Technology**

RTL8111C

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**USB\_LAN CONNECTOR**

LAN 100歐姆:[30/4/8/4/30] FOR B 製程

USB\_LAN

LBC27 100p4/NPO/50V/U/X

P\_M0D+ L1  
P\_M0D- L2  
P\_M0H+ L3  
P\_M0H- L4  
P\_M0L+ L5  
P\_M0L- L6  
P\_M0B+ L7  
P\_M0B- L8  
P\_M0G+ L9  
P\_M0G- L10

LBC34 100p4/NPO/50V/U

UP

DOWN

USB+LAN1G/GQ.YIOS/RA/D/8C  
8 Core

90歐姆:[15/4.5/7.5/4.5/15]

**USB POWER**

3VDUAL

LFB5 0/6/SHT/X

LBC33 0.1u4/X7R/16V/K

FUSEVCC1

LBC26 0.1u4/X7R/16V/K

D1 LED\_ACT\_TXRX  
D2 LED\_GREEN  
D3 LED\_LINK1000  
D4 LED\_LINK1000

LR17 150/6  
LR19 150/6  
LR18 150/6

U1 -USBP5  
U2 +USBP5 19  
U3 -USBP5  
U4 +USBP5 19  
U5 -USBP5  
U6 +USBP5 19  
U7 -USBP4  
U8 +USBP4 19

**USB\_LAN**

REAR USB OC

5VDUAL

R27 150K/4

R22 270K/4

FUSEVCC1

BAT54A/SOT23/200mA/[10DK1-320054-12R]

D1

USB\_OC\_R -USB\_OC\_R 19

**Gigabyte Technology**

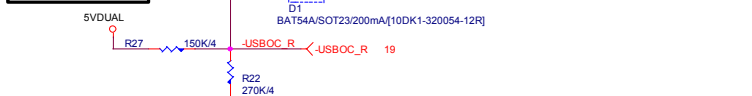
RTL8111C

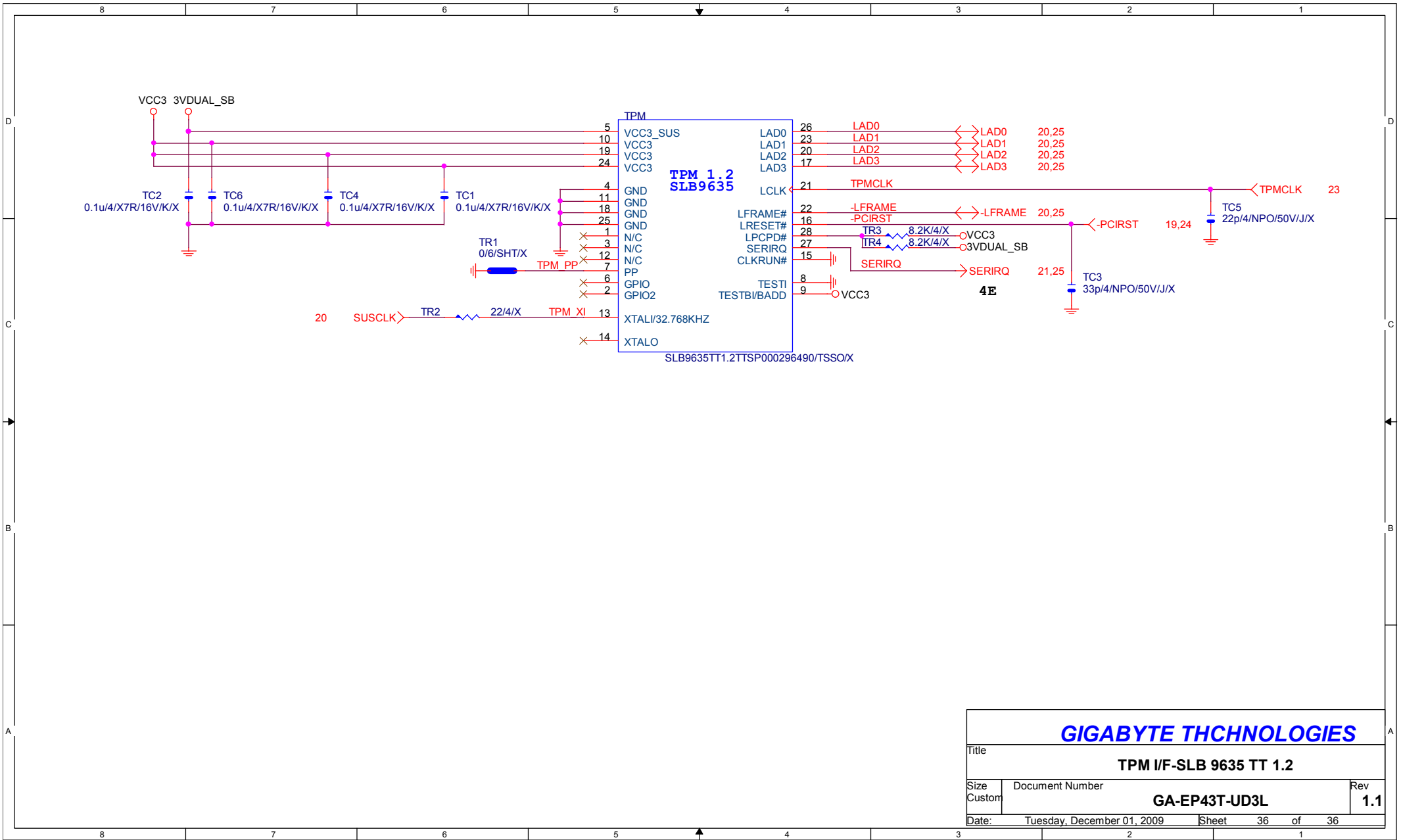
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**GIGABYTE THCHNOLOGIES**

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